

SEMiX® 5

Bridge Rectifier Module (halfcontrolled)

SEMiX245DH16

Features

- Terminal height 17 mm
- Solderless assembling solution with PressFIT signal pins and screw power terminals
- NTC temperature sensor inside

Typical Applications*

- Input Bridge Rectifier for AC/DC motor control
- Power supply

Remarks

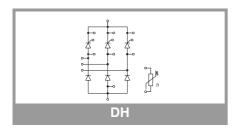
- Reliability tests performed at T_i = 130°C
- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
Module	Module						
I _D	T _j = 130 °C	T _c = 96 °C	336	Α			
	rec. 120°	T _c = 80 °C	440	Α			
T _{stg}	module without	TIM	-40 125	°C			
V _{isol}	AC sinus 50Hz	t = 1 min	4000	V			

Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
Thyristor			•				
I _{T(AV)}	T _j = 130 °C	T _c = 80 °C	154	Α			
	sinus 180°	T _c = 100 °C	107	Α			
I _{TSM}	10 ms	T _j = 25 °C	2050	Α			
TOTALS	101115	T _j = 130 °C	1800	Α			
i ² t	10 ms	T _j = 25 °C	21013	A ² s			
	101115	T _j = 130 °C	16200	A ² s			
V_{RSM}			1700	V			
V_{RRM}			1600	V			
V_{DRM}			1600	V			
(di/dt) _{cr}	T _j = 130 °C		100	A/μs			
(dv/dt) _{cr}	T _j = 130 °C		1000	V/µs			
Tj			-40 130	°C			

Absolute	Maximum Rati	ngs		
Symbol	Conditions		Values	Unit
Diode	•		•	
I _{FAV}	T _j = 150 °C	T _c = 80 °C	167	Α
	sin. 180°	T _c = 100 °C	135	Α
I _{FSM}	10 ms	T _j = 25 °C	2100	Α
	101115	T _j = 130 °C	1700	Α
i ² t	10 ms	T _j = 25 °C	22050	A ² s
	101115	T _j = 130 °C	14450	A ² s
V _{RSM}			1700	V
V_{RRM}			1600	V
Tj			-40 150	°C

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Temperature Sensor							
R ₁₀₀	T _c =100°C (R ₂₅ =5 kΩ)	493 ± 5%			Ω		
B _{100/125}	$R_{(T)} = R_{100} exp[B_{100/125}(1/T-1/T_{100})]; T[K];$	3550 ±2%		К			





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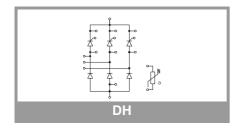
Remarks

- Reliability tests performed at T_j = 130°C
- For storage and case temperature with TIM see document "TP(HALA P8) SEMiX 5p"

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Thyristor							
V_{T}	$T_j = 130 ^{\circ}\text{C}$, $I_T = 140 \text{A}$, chiplevel		1.10	1.17	V		
$V_{T(TO)}$	T _j = 130 °C, chiplevel		0.84	0.91	V		
r _T	T _j = 130 °C, chiplevel		1.85	1.87	mΩ		
$I_{DD};I_{RD}$	$T_j = 130 ^{\circ}\text{C}, V_{DD} = V_{DRM}; V_{RD} = V_{RRM}$			21	mA		
t _{gd}	$T_j = 25 ^{\circ}\text{C}$, $I_G = 1 \text{A}$, $di_G/dt = 1 \text{A}/\mu\text{s}$		1		μs		
t _{gr}	$V_{D} = 0.67 * V_{DRM}$		2		μs		
t _q	T _j = 130 °C		150		μs		
I _H	T _j = 25 °C		150	220	mA		
IL	$T_j = 25$ °C, $R_G = 33 \Omega$		300	550	mA		
V_{GT}	$T_j = 25$ °C, d.c.	1.65			V		
I_{GT}	$T_j = 25$ °C, d.c.	100			mA		
V_{GD}	T _j = 130 °C, d.c.			0.25	V		
I_{GD}	T _j = 130 °C, d.c.			3.8	mA		
R _{th(j-c)}	per thyristor, sin. 180°			0.2	K/W		
R _{th(c-s)}	per thyristor (λ _{grease} =0.81 W/(m*K))		0.072		K/W		
R _{th(c-s)}	per thyristor, pre-applied phase change material		0.053		K/W		

Characteristics							
Symbol	Conditions		min.	typ.	max.	Unit	
Diode						•	
V _F	I _F = 140 A	T _j = 25 °C		1.04	1.28	V	
	chiplevel	T _j = 150 °C		0.95	1.19	V	
$V_{(TO)}$	chiplevel	T _j = 25 °C		0.88	0.98	V	
	Criipievei	T _j = 125 °C		0.73	0.83	V	
r _T	chiplevel	T _j = 25 °C		1.13	2.2	mΩ	
		T _j = 125 °C		1.60	2.5	mΩ	
I _{RD}	T _j = 130 °C, V _{RD} = V _{RRM}				2	mA	
R _{th(j-c)}	per diode, sin. 180			0.22	K/W		
R _{th(c-s)}	per Diode (λ _{grease} =0.81 W/(m*K))			0.072		K/W	
R _{th(c-s)}	per Diode, pre-app material		0.053		K/W		

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
Module							
L _{CE}				20		nH	
R _{CC'+EE'}	measured per	T _C = 25 °C	0.8			mΩ	
	switch	T _C = 125 °C		mΩ			
Rth _{(c-s)1}	calculated without thermal coupling		0.012			K/W	
Rth _{(c-s)2}	including thermal coupling, Ts underneath module (λ_{grease} =0.81 W/ (m*K))			0.020		K/W	
Rth _{(c-s)1}	calculated without thermal coupling; pre-applied phase change material			0.009		K/W	
Rth _{(c-s)2}	including thermal coupling, Ts underneath module, pre-applied phase change material			0.015		K/W	
Ms	to heat sink (M5)		3		6	Nm	
M _t	to terminals (M6)		3		6	Nm	
W				398		g	



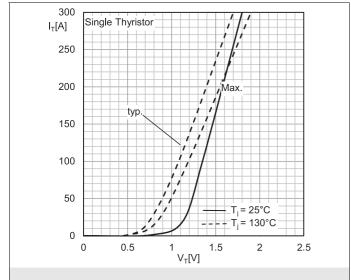


Fig. 1: Thyristor typ. on-state characteristic, incl. R_{CC'+ EE'}

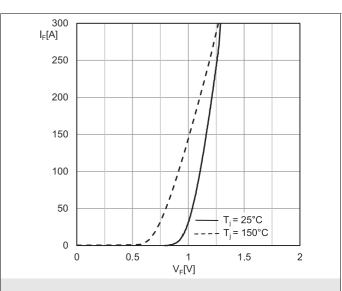


Fig. 2: Diode typ. on-state characteristic, incl. R_{CC'+ EE'}

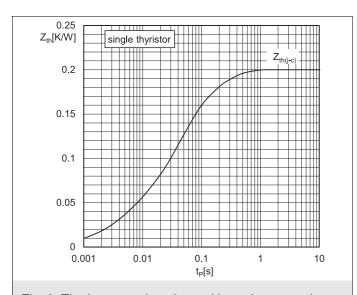


Fig. 3: Thyristor transient thermal impedance vs. time

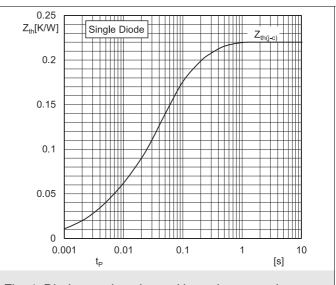


Fig. 4: Diode transient thermal impedance vs. time

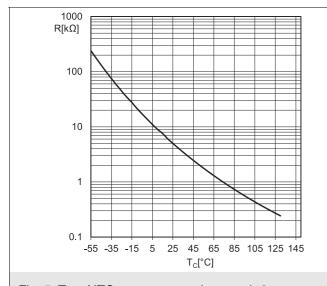
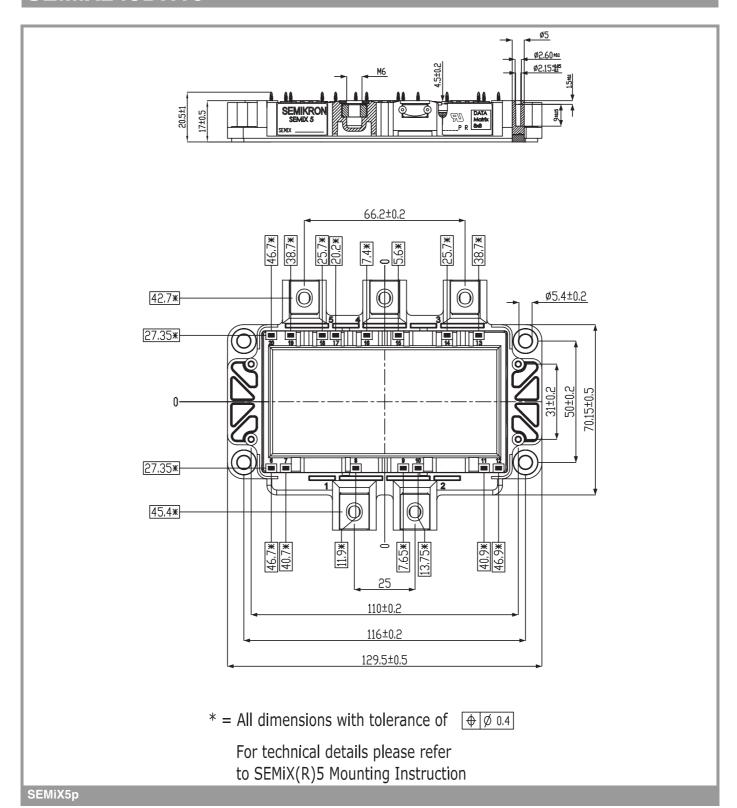
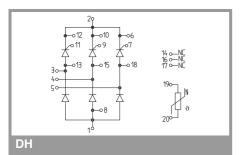


Fig. 5: Typ. NTC-temperature characteristics





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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