### MDD310-12N1

## **Standard Rectifier Module**

Phase le	eg
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Part number

MDD310-12N1





Backside: isolated **E**72873



### Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- · Very low leakage current

### **Applications:**

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- · Field supply for DC motors

#### Package: Y2

- Isolation Voltage: 3600 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office. Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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Rectifier					Rating	S	
Symbol	Definition	Conditions		min.	typ.	max.	Unit
V <sub>RSM</sub>	max. non-repetitive reverse bloc	king voltage	$T_{VJ} = 25^{\circ}C$			1300	V
V <sub>RRM</sub>	max. repetitive reverse blocking	voltage	$T_{VJ} = 25^{\circ}C$			1200	V
I <sub>R</sub>	reverse current	V <sub>R</sub> = 1200 V	$T_{VJ} = 25^{\circ}C$			1	mA
		$V_{R} = 1200 V$	$T_{vJ} = 150^{\circ}C$			20	mA
V <sub>F</sub>	forward voltage drop	I <sub>F</sub> = 300 A	$T_{VJ} = 25^{\circ}C$			1.13	V
		I <sub>F</sub> = 600 A				1.33	V
		I <sub>F</sub> = 300 A	T <sub>vJ</sub> = 125 °C			1.00	V
		$I_{F} = 600 \text{ A}$				1.29	V
FAV	average forward current	T <sub>c</sub> = 100°C	$T_{VJ} = 150^{\circ}C$			300	A
F(RMS)	RMS forward current	180° sine				480	Α
V <sub>F0</sub>	threshold voltage		$T_{VJ} = 150^{\circ}C$			0.75	V
r <sub>F</sub>	slope resistance } for power	loss calculation only				0.63	mΩ
<b>R</b> <sub>thJC</sub>	thermal resistance junction to ca	ase				0.13	K/W
R <sub>thCH</sub>	thermal resistance case to heats	sink			0.04		K/W
P <sub>tot</sub>	total power dissipation		$T_c = 25^{\circ}C$			960	W
I <sub>FSM</sub>	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			11.5	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			12.4	kA
		t = 10 ms; (50 Hz), sine	$T_{VJ} = 150^{\circ}C$			9.78	kA
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			10.6	kA
l²t	value for fusing	t = 10 ms; (50 Hz), sine	$T_{VJ} = 45^{\circ}C$			661.3	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			641.7	kA²s
		t = 10 ms; (50 Hz), sine	$T_{vJ} = 150^{\circ}C$			477.8	kA²s
		t = 8,3 ms; (60 Hz), sine	$V_{R} = 0 V$			463.5	kA²s
C	junction capacitance	V <sub>R</sub> = 400 V; f = 1 MHz	$T_{vJ} = 25^{\circ}C$		381		pF

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Package Y2		Ratings					
Symbol	Definition	Conditions		min.	typ.	max.	Unit
I <sub>RMS</sub>	RMS current	per terminal				600	Α
T <sub>vJ</sub>	virtual junction temperature			-40		150	°C
T <sub>op</sub>	operation temperature			-40		125	°C
T <sub>stg</sub>	storage temperature			-40		125	°C
Weight					254		g
M <sub>D</sub>	mounting torque			2.5		5	Nm
M <sub>T</sub>	terminal torque			12		15	Nm
d <sub>Spp/App</sub>	creenade distance on surfa	ce   striking distance through air	terminal to terminal	13.0			mm
<b>d</b> <sub>Spb/Apb</sub>	creepage distance on surface   striking distance through air		terminal to backside	13.0			mm
V	isolation voltage	t = 1 second		3600			۷
		t = 1 minute	50/60 Hz, RMS; liso∟ ≤ 1 mA	3000			V



Date Code (DC) + Production Index (PI)

Data Matrix: part no. (1-19), DC + PI (20-25), lot.no.# (26-31), blank (32), serial no.# (33-36)

Order	ing	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Stand	ard	MDD310-12N1	MDD310-12N1	Box	2	429155

Equiva	alent Circuits for	Simulation	* on die level	$T_{vJ} = 150 \ ^{\circ}C$
	) Ro	Rectifier		
V <sub>0 max</sub>	threshold voltage	0.75		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	0.4		mΩ

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### Outlines Y2







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#### Rectifier



Fig.6 Three phase rectifier bridge: Power dissipation versus direct output current and ambient temperature



R <sub>thJC</sub> f	or various conduction angles d
d	R <sub>thJC</sub> [K/W]
	0 129

 DC
 0.129

 180°
 0.131

 120°
 0.132

 60°
 0.132

 30°
 0.133

Constants for  $Z_{thJC}$  calculation:

i	R <sub>thi</sub> [K/W]	t <sub>i</sub> [s]
1	0.0035	0.0099
2	0.0165	0.1680
3	0.1091	0.4560

Fig. 7 Transient thermal impedance junction to case (per diode)



 $\mathbf{R}_{\text{thJK}}$  for various conduction angles d

d	R <sub>thJK</sub> [K/W]
DC	0.169
180°	0.171
120°	0.172
60°	0.172
30°	0.173

Constants for  $\boldsymbol{Z}_{thJK}$  calculation:

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i	R <sub>thi</sub> (K/W)	t <sub>i</sub> (s)
1	0.0035	0.0099
2	0.0165	0.1680
3	0.1091	0.4560
4	0.0400	1.3600

Fig. 8 Transient thermal impedance junction to heatsink (per diode)

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