



Date:- 13 Oct 2004

Data Sheet Issue:- 1

Rectifier Diode

Types W1074Y#200 to W1074Y#320

Old Type No.: SW20-32CXC445

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	2000-3200	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	2100-3300	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=55^{\circ}C$, (note 2)	1074	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$, (note 2)	736	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$, (note 3)	444	A
$I_{F(RMS)M}$	Nominal RMS forward current, $T_{sink}=25^{\circ}C$, (note 2)	1984	A
$I_{F(d.c.)}$	D.C. forward current, $T_{sink}=25^{\circ}C$, (note 4)	1704	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	10.8	kA
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	11.8	kA
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	583×10^3	A ² s
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	706×10^3	A ² s
$T_{j op}$	Operating temperature range	-40 to +160	°C
T_{stg}	Storage temperature range	-40 to +200	°C

Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T_j below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, 160°C T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V_{FM}	Maximum peak forward voltage	-	-	2.13	$I_{FM}=3090A$	V
V_{TO}	Threshold voltage	-	-	0.92		V
r_T	Slope resistance	-	-	0.39		$m\Omega$
I_{RRM}	Peak reverse current	-	-	30	Rated V_{RRM}	mA
I_{RRM}	Peak reverse current	-	-	30	Rated V_{RRM} , $T_j=25^\circ C$	mA
Q_{rr}	Recovered charge	-	1800	-		μC
Q_{ra}	Recovered charge, 50% Chord	-	900	1450		μC
I_{rr}	Reverse recovery current	-	100	-		A
t_{rr}	Reverse recovery time	-	18	-		μs
R_{thJK}	Thermal resistance, junction to heatsink	-	-	0.05	Double side cooled	K/W
		-	-	0.10	Single side cooled	K/W
F	Mounting force	5.5	-	8.3		kN
W_t	Weight	-	90	-	Housing option YC	g
		-	140	-	Housing option YH	

Notes:-

1) Unless otherwise indicated $T_j=160^\circ C$.

Notes on Ratings and Characteristics**1.0 Voltage Grade Table**

Voltage Grade	V_{DRM} V	V_{DSM} V	V_{RRM} V	V_{RSM} V	V_D DC V	V_R DC V
20	2000			2100		1250
22	2200			2300		1350
24	2400			2500		1450
26	2600			2700		1550
28	2800			2900		1650
30	3000			3100		1750
32	3200			3300		1850

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters**5.1 Device Dissipation Calculations**

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}} \quad \Delta T = T_{j\max} - T_K$$

Where $V_{T0}=0.92V$, $r_T=0.39m\Omega$,

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance				
Conduction Angle	6 Phase (60°)	3 Phase (120°)	½ Wave (180°)	d.c.
Square wave Double Side Cooled	0.069	0.061	0.057	0.05
Square wave Single Side Cooled	0.119	0.111	0.107	0.1
Sine wave Double Side Cooled	0.052	0.0513	0.0505	
Sine wave Single Side Cooled	0.102	0.1013	0.1005	

Form Factors				
Conduction Angle	6 Phase (60°)	3 Phase (120°)	½ Wave (180°)	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F , on page 6 is represented in two ways;

- (i) the well established V_{TO} and r_T tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	160°C Coefficients
A	0.935427	0.306711779
B	-0.02290183	0.09325144
C	1.301×10^{-4}	3.37364×10^{-4}
D	0.01538908	5.66152×10^{-4}

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}} \right)$$

Where $p = 1$ to n , n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r_t = Thermal resistance at time t .

r_p = Amplitude of p_{th} term.

τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Single Side Cooled					
Term	1	2	3	4	5
r_p	0.06158	8.43118×10^{-3}	0.01031	0.01614	5.0391×10^{-3}
τ_p	2.13613	1.2129	0.15024	0.04244	3.6777×10^{-3}

D.C. Double Side Cooled				
Term	1	2	3	4
r_p	0.02	9.9234×10^{-3}	0.01434	4.284×10^{-3}
τ_p	0.33917	0.12691	0.03562	2.563×10^{-3}

6.0 Reverse recovery ratings

(i) Q_{rr} is based on 50% I_{rm} chord as shown in Fig. 1

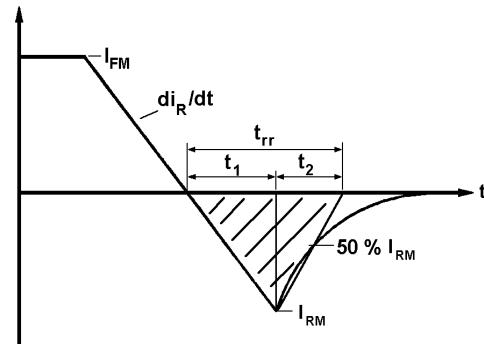


Fig. 1

(ii) Q_{rr} is based on a 150μs integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} dt$$

(iii)

$$K \text{ Factor} = \frac{t_1}{t_2}$$

Curves

Figure 1 - Forward characteristics of Limit device

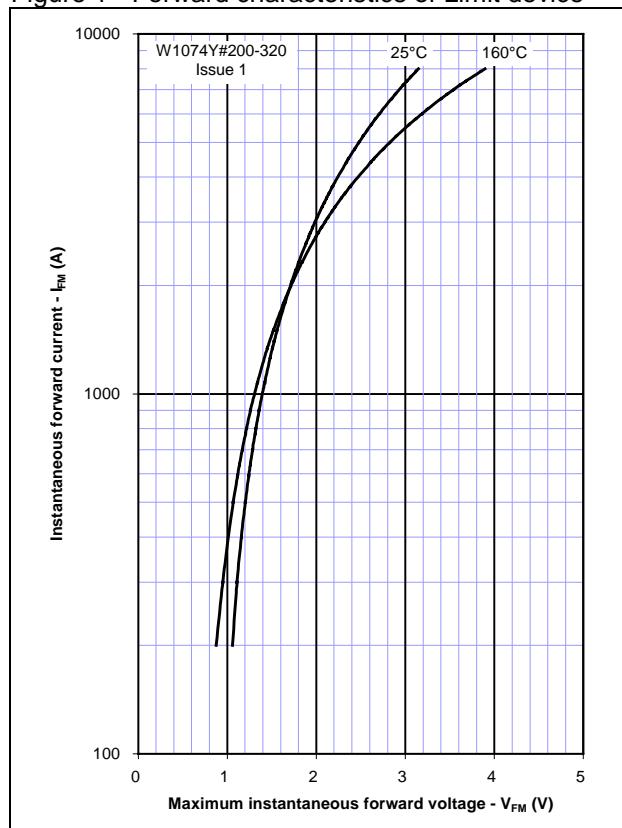


Figure 2 - Transient thermal impedance

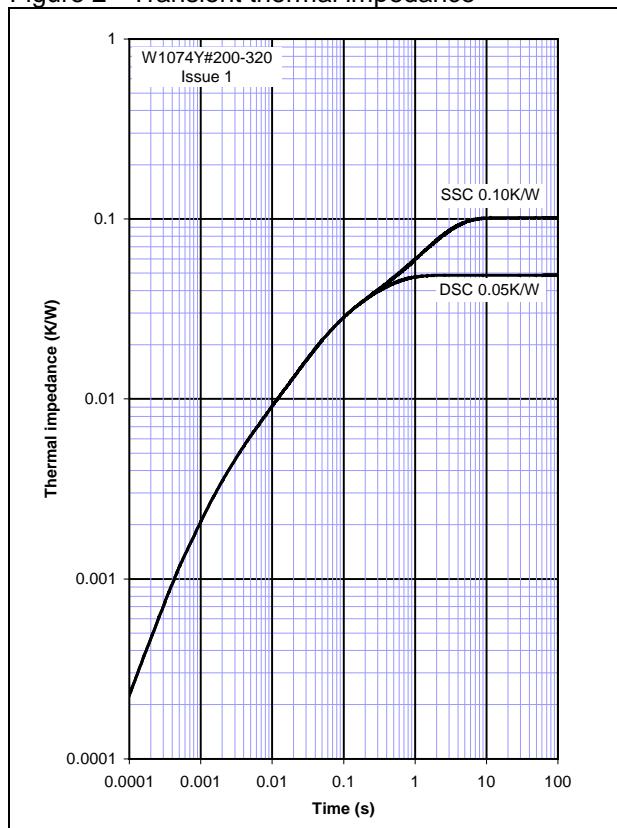


Figure 3 - Maximum surge Rating

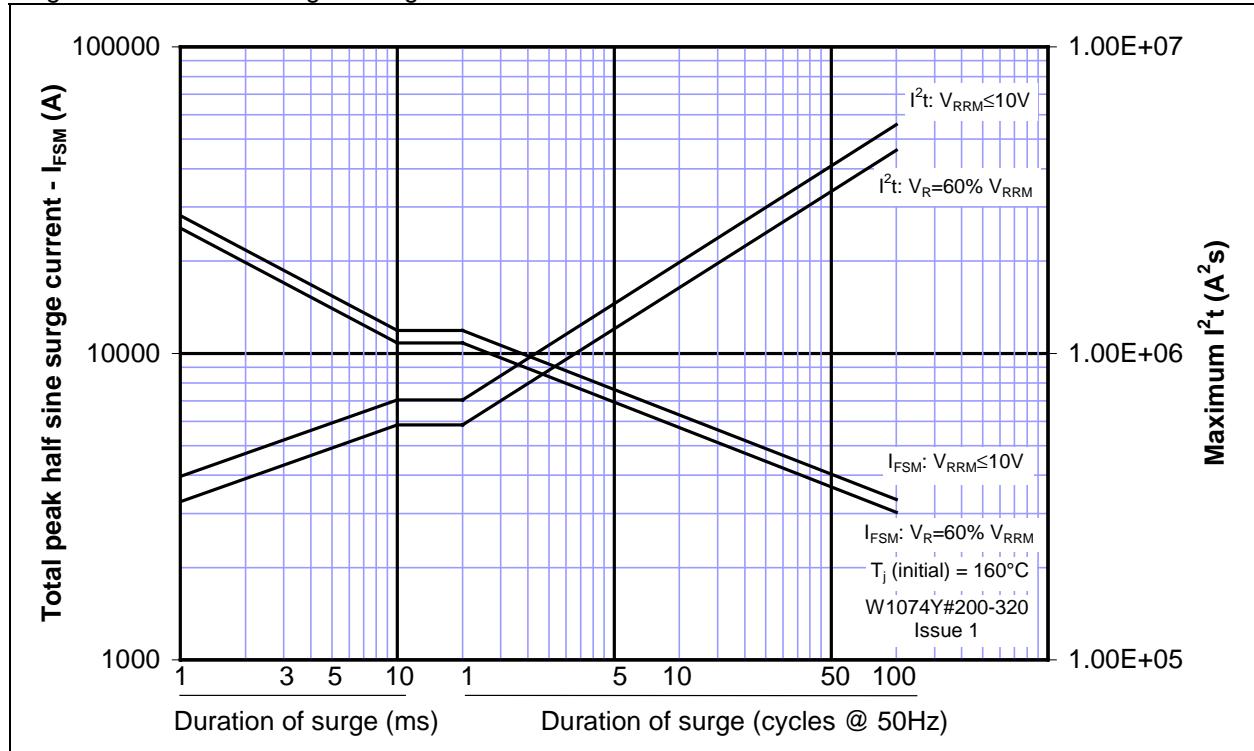


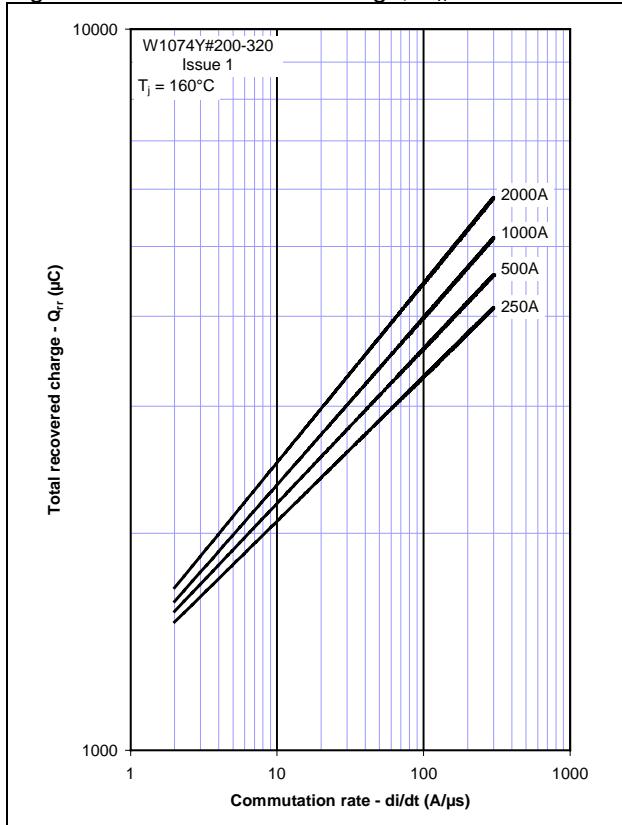
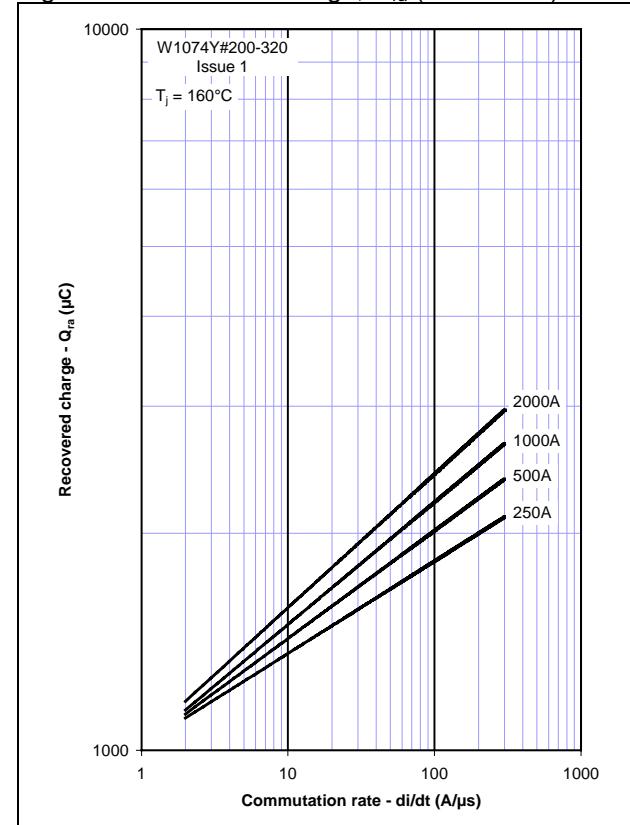
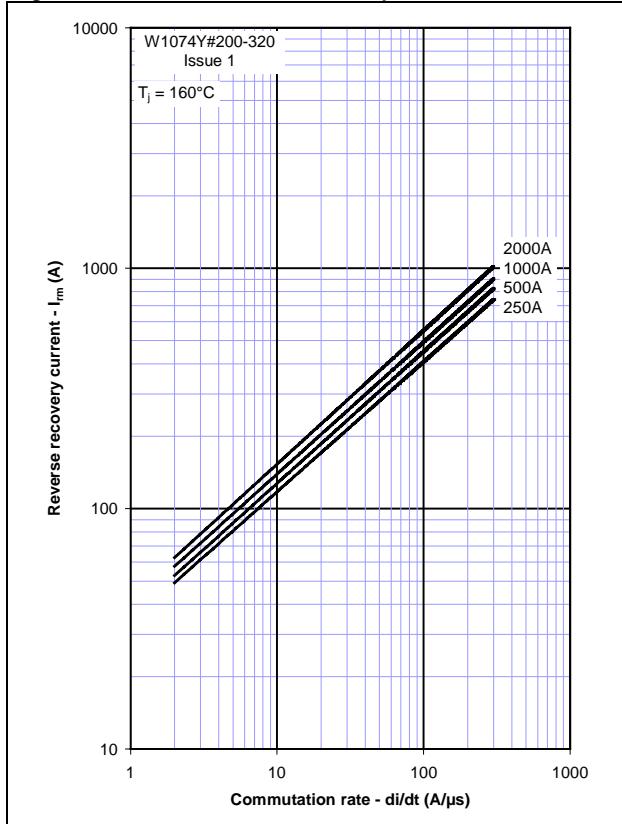
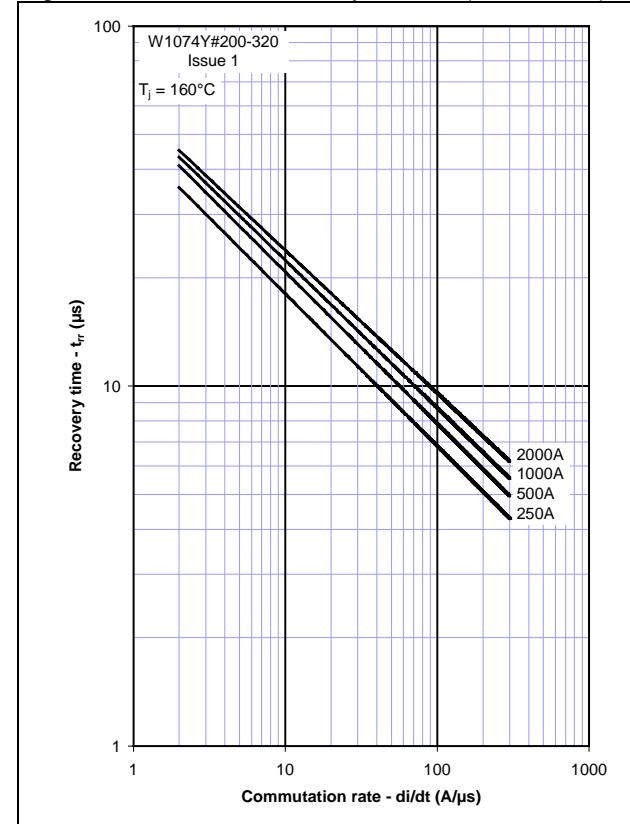
Figure 4 - Total recovered charge, Q_{rr} Figure 5 - Recovered charge, Q_{ra} (50% chord)Figure 6 - Peak reverse recovery current, I_{rm} Figure 7 - Maximum recovery time, t_{rr} (50% chord)

Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

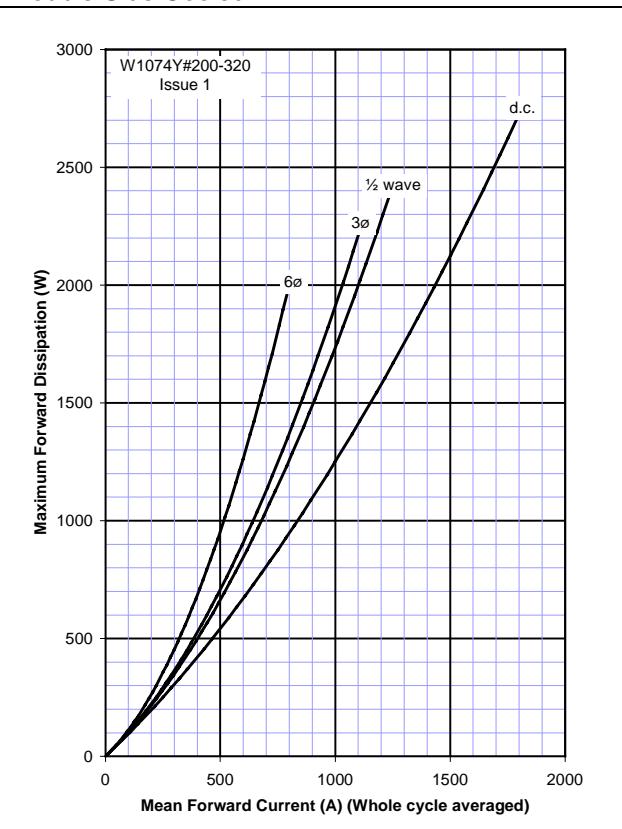


Figure 9 – Forward current vs. Heatsink temperature - Double Side Cooled

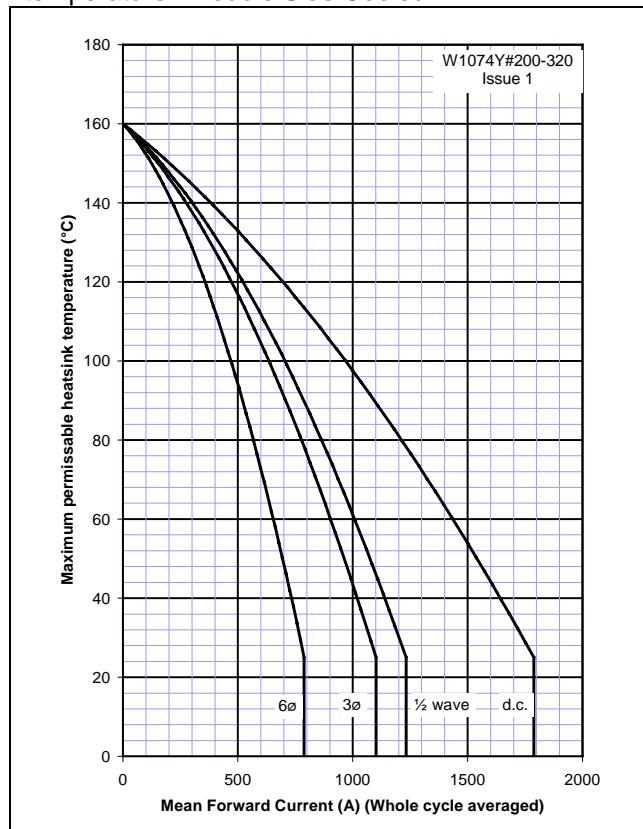


Figure 10 – Forward current vs. Power dissipation – Single Side Cooled

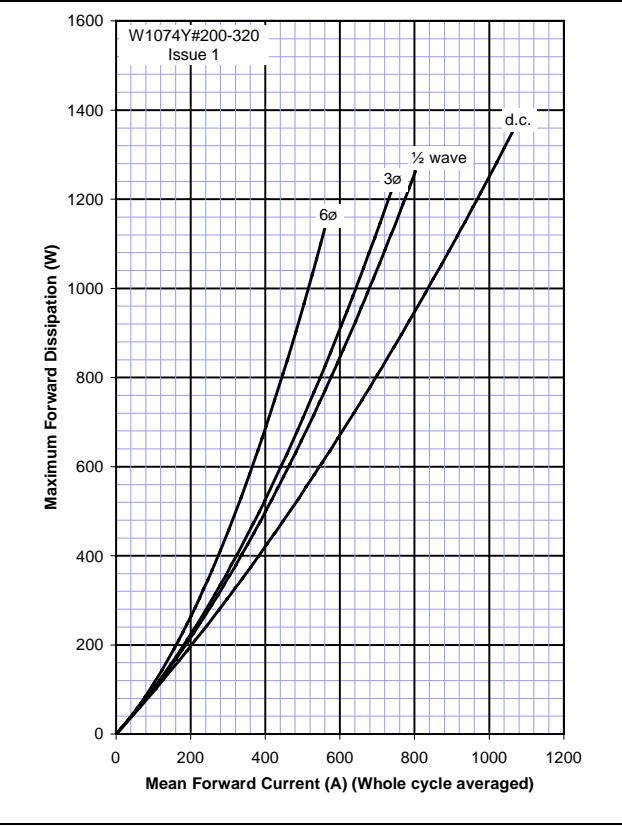
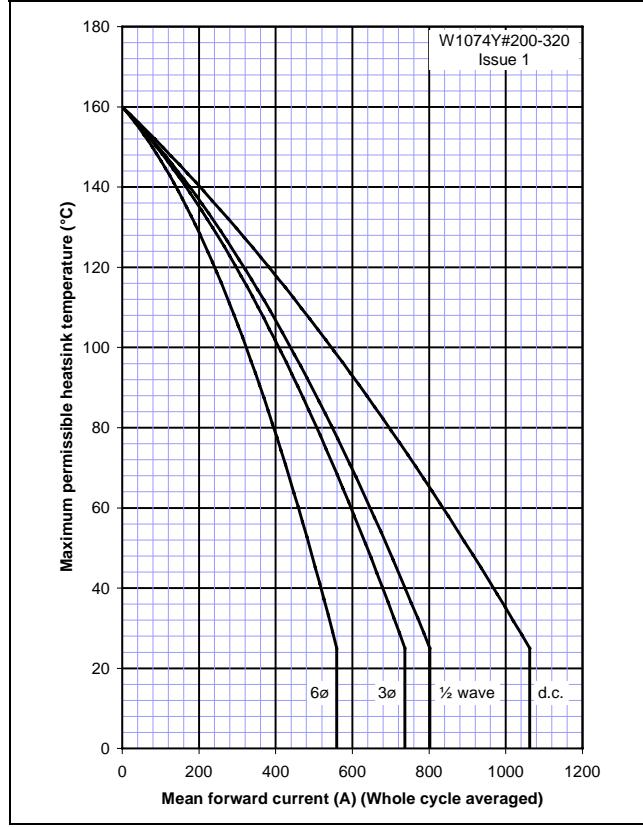
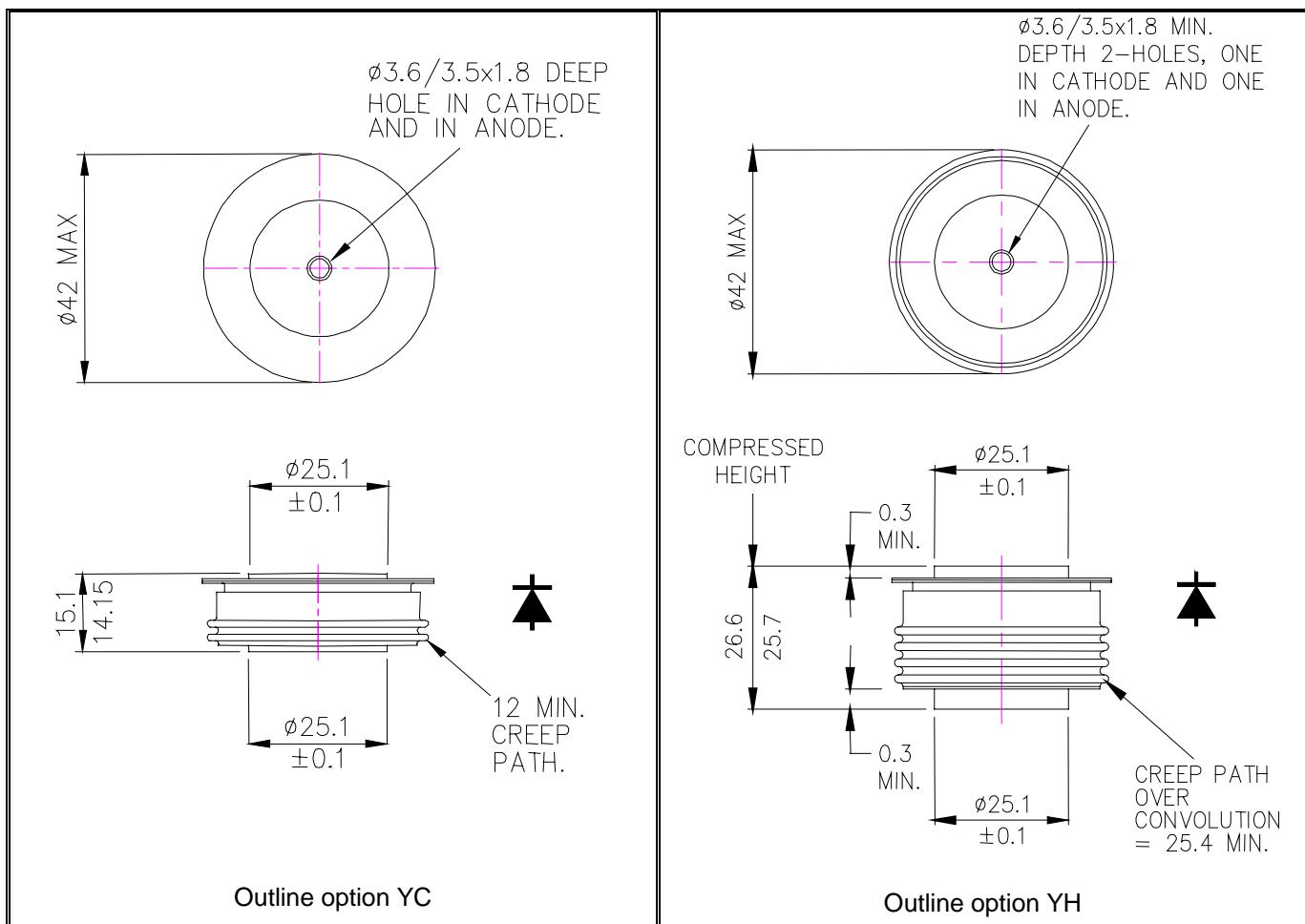


Figure 11 – Forward current vs. Heatsink temperature – Single Side Cooled



Outline Drawing & Ordering Information

Outline option YC

Outline option YH

ORDERING INFORMATION

(Please quote 10 digit code as below)

W1074**Y#****♦♦****0**Fixed
Type CodeFixed outline code
YC = 15.1mm Clamp Height
YH = 26.6mm Clamp HeightVoltage code
 $V_{DRM}/100$
20-32

Fixed code

Order code: W1074YH240 – 2400V V_{RRM} , 26.6mm clamp height capsule.

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