

Rectifier Diode

Types W2820V#360 to W2820V#450

Old Type No.: SW36-45C/FXC1100

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{RRM}	Repetitive peak reverse voltage, (note 1)	3600-4500	V
V_{RSM}	Non-repetitive peak reverse voltage, (note 1)	3700-4600	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, $T_{sink}=55^{\circ}C$, (note 2)	2820	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$, (note 2)	1887	A
$I_{F(AV)M}$	Maximum average forward current. $T_{sink}=100^{\circ}C$, (note 3)	1101	A
$I_{F(RMS)M}$	Nominal RMS forward current, $T_{sink}=25^{\circ}C$, (note 2)	5262	A
$I_{F(d.c.)}$	D.C. forward current, $T_{sink}=25^{\circ}C$, (note 4)	4350	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	26.2	kA
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	28.8	kA
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}=60\%V_{RRM}$, (note 5)	3.43×10^6	A^2s
I^2t	I^2t capacity for fusing $t_p=10ms$, $V_{rm}\leq 10V$, (note 5)	4.15×10^6	A^2s
$T_{j\ op}$	Operating temperature range	-40 to +160	$^{\circ}C$
T_{stg}	Storage temperature range	-40 to +160	$^{\circ}C$

Notes:-

- 1) De-rating factor of 0.13% per $^{\circ}C$ is applicable for T_j below $25^{\circ}C$.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave, $160^{\circ}C$ T_j initial.

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS
V _{FM}	Maximum peak forward voltage	-	-	2.3	I _{TM} =6800A	V
V _{FM}	Maximum peak forward voltage	-	-	2.6	I _{TM} =8460A	V
V _{T0}	Threshold voltage	-	-	1.3		V
r _T	Slope resistance	-	-	0.147		mΩ
I _{RRM}	Peak reverse current	-	-	60	Rated V _{RRM}	mA
Q _{rr}	Recovered charge	-	7400	-		μC
Q _{ra}	Recovered charge, 50% Chord	-	4000	4600	I _{TM} =1000A, t _p =1000μs, di/dt=10A/μs, V _r =50V	μC
I _{rr}	Reverse recovery current	-	190	-		A
t _{rr}	Reverse recovery time, 50% Chord	-	42	-		μs
R _{thJK}	Thermal resistance, junction to heatsink	-	-	0.016	Double side cooled	K/W
		-	-	0.032	Single side cooled	K/W
F	Mounting force	27	-	34		kN
W _t	Weight	-	1000	-	Outline Options VC	g
		-	800	-	Outline Options VF	

Notes:-

- 1) Unless otherwise indicated T_j=160°C.
- 2) For other clamp forces, please consult factory.

Notes on Ratings and Characteristics1.0 Voltage Grade Table

Voltage Grade	V_{DRM} V_{DSM} V_{RRM} V	V_{RSM} V	V_D V_R DC V
36	3600	3700	1900
38	3800	3900	1950
40	4000	4100	2000
42	4200	4300	2040
44	4400	4500	2080
45	4500	4600	2120

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_j below 25°C.

4.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

5.0 Computer Modelling Parameters

5.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^2 + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T} \quad \text{and:} \quad W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\max} - T_K$$

Where $V_{T0}=1.3V$, $r_T=0.147m\Omega$,

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave Double Side Cooled	0.01576	0.01487	0.01429	0.01328
Square wave Single Side Cooled	0.03399	0.03348	0.03300	0.03174
Sine wave Double Side Cooled	0.01507	0.01429	0.01351	
Sine wave Single Side Cooled	0.03371	0.03318	0.03246	

Form Factors				
Conduction Angle	6 phase (60°)	3 phase (120°)	½ wave (180°)	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

5.2 Calculating V_F using ABCD Coefficients

The on-state characteristic I_F vs. V_F , on page 6 is represented in two ways;

- (i) the well established V_{T0} and r_f tangent used for rating purposes and
- (ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_F in terms of I_F given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_F agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	160°C Coefficients
A	0.82003623	0.489599427
B	0.03242465	0.06173783
C	9.876×10^{-5}	1.50996×10^{-4}
D	3.28948×10^{-3}	2.896029×10^{-3}

5.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{-\frac{t}{\tau_p}} \right)$$

Where $p = 1$ to n , n is the number of terms in the series and:

t = Duration of heating pulse in seconds.

r_t = Thermal resistance at time t .

r_p = Amplitude of p_{th} term.

τ_p = Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Single Side Cooled		
Term	1	2
r_p	0.54145	2.62145
τ_p	0.07599	0.03111

D.C. Double Side Cooled				
Term	1	2	3	4
r_p	0.06465	0.04241	0.02065	5.09×10^{-3}
τ_p	0.29708	0.10877	0.04414	2.95×10^{-3}

6.0 Reverse recovery ratings

(i) Q_{rr} is based on 50% I_{RM} chord as shown in Fig. 1

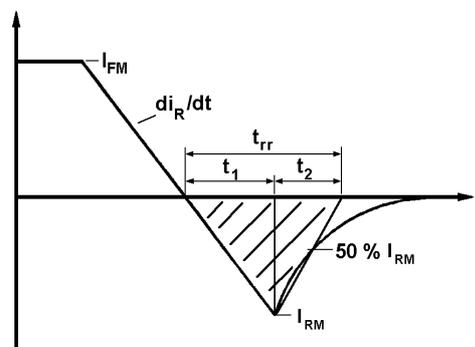


Fig. 1

(ii) Q_{rr} is based on a $150\mu s$ integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr} \cdot dt$$

(iii) $K \text{ Factor} = \frac{t_1}{t_2}$

Curves

Figure 1 - Forward characteristics of Limit device

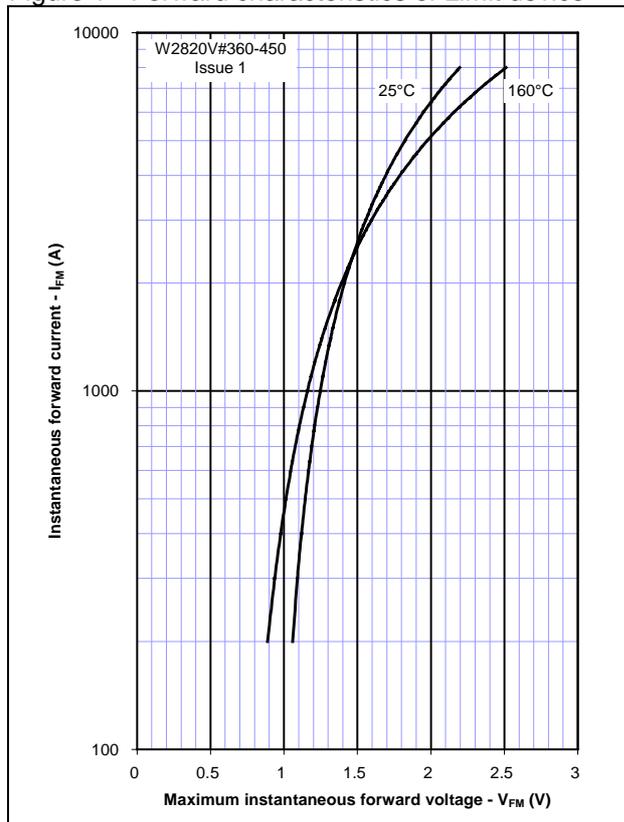


Figure 2 - Transient thermal impedance

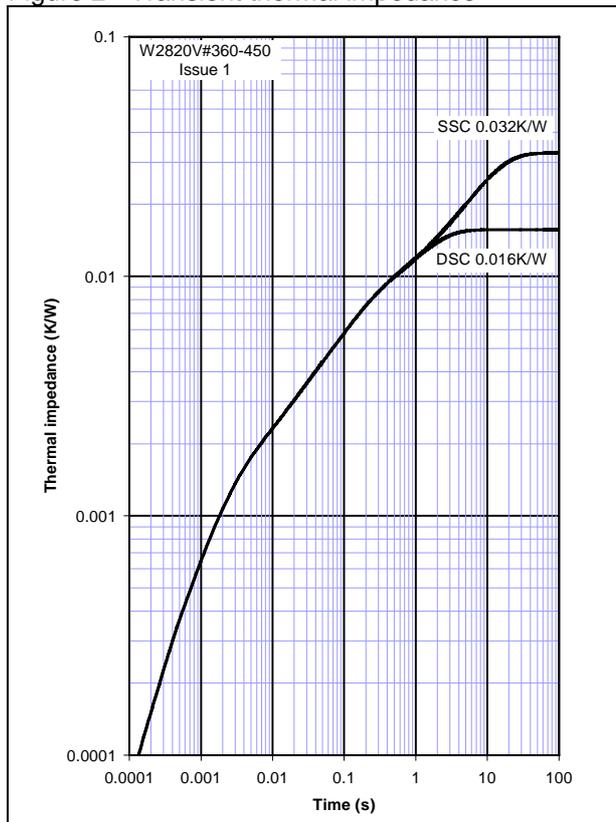


Figure 3 - Maximum surge Rating

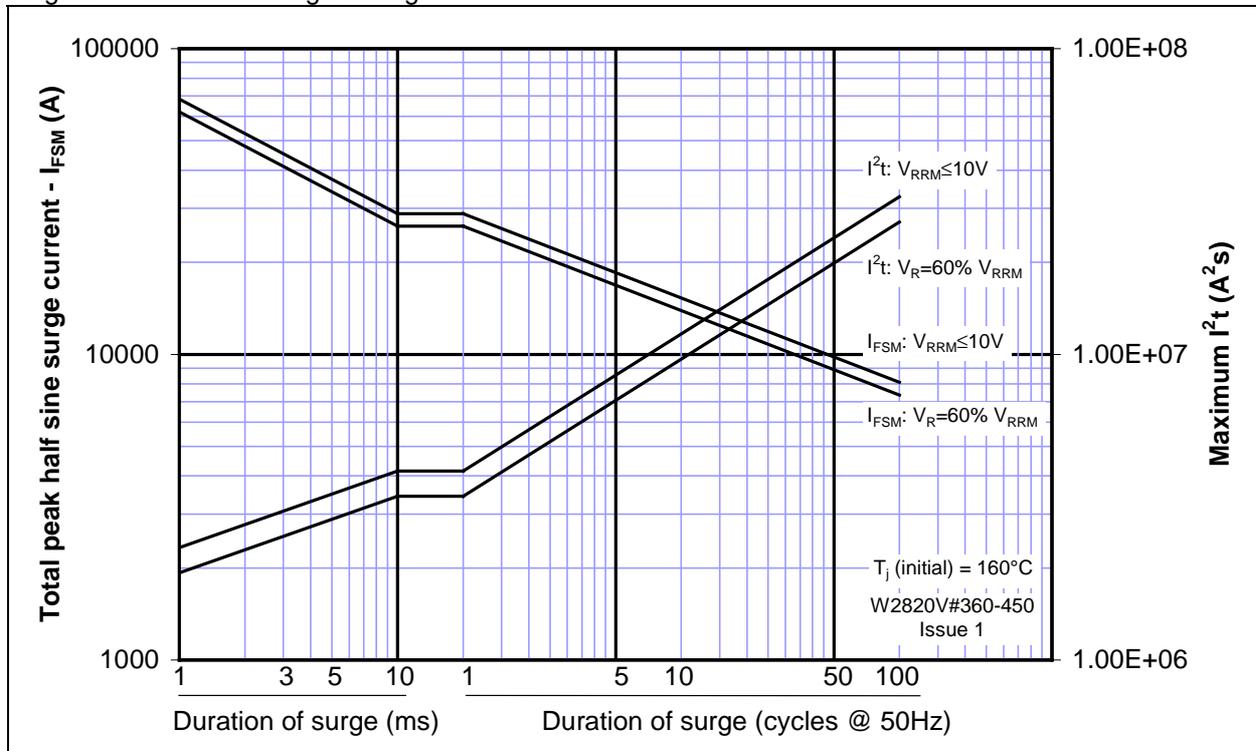


Figure 4 - Total recovered charge, Q_{rr}

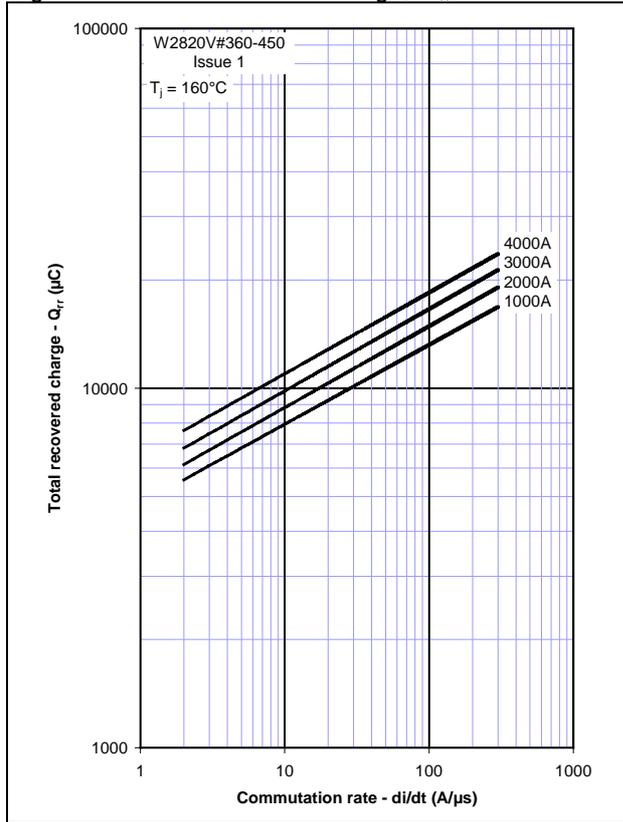


Figure 5 - Recovered charge, Q_{ra} (50% chord)

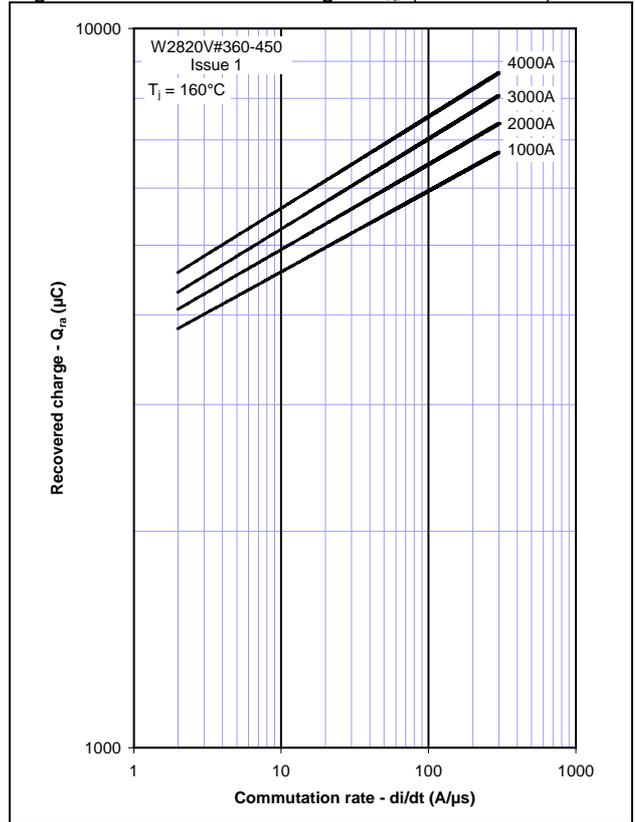


Figure 6 - Peak reverse recovery current, I_{rm}

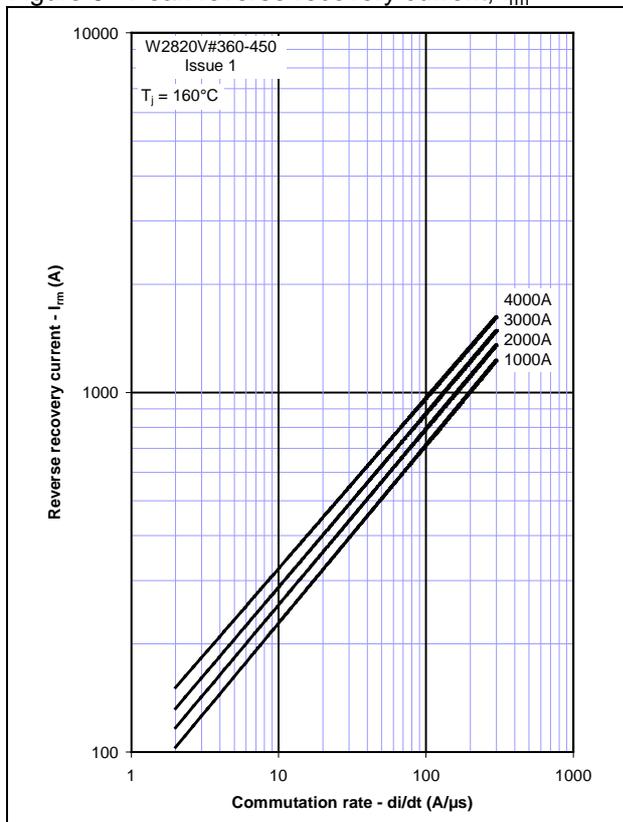


Figure 7 - Maximum recovery time, t_{rr} (50% chord)

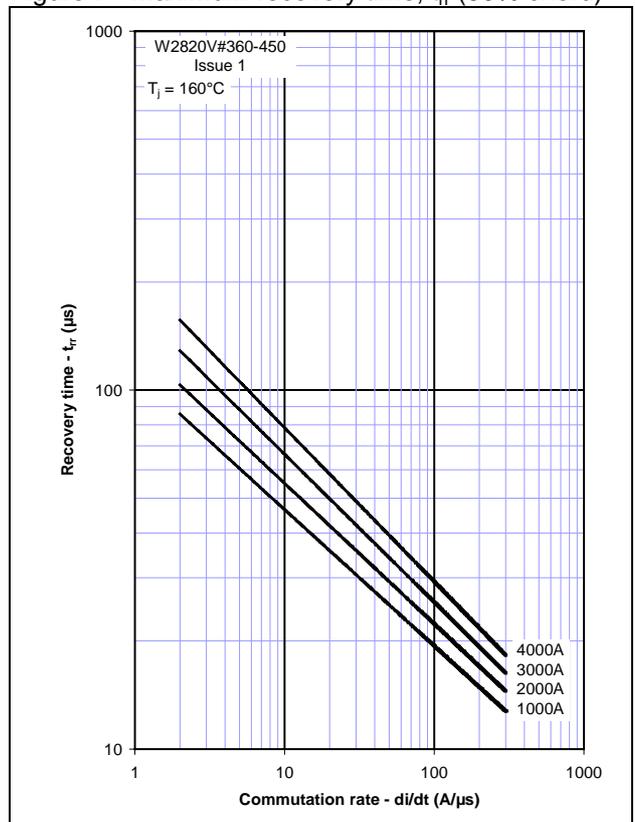


Figure 8 – Forward current vs. Power dissipation – Double Side Cooled

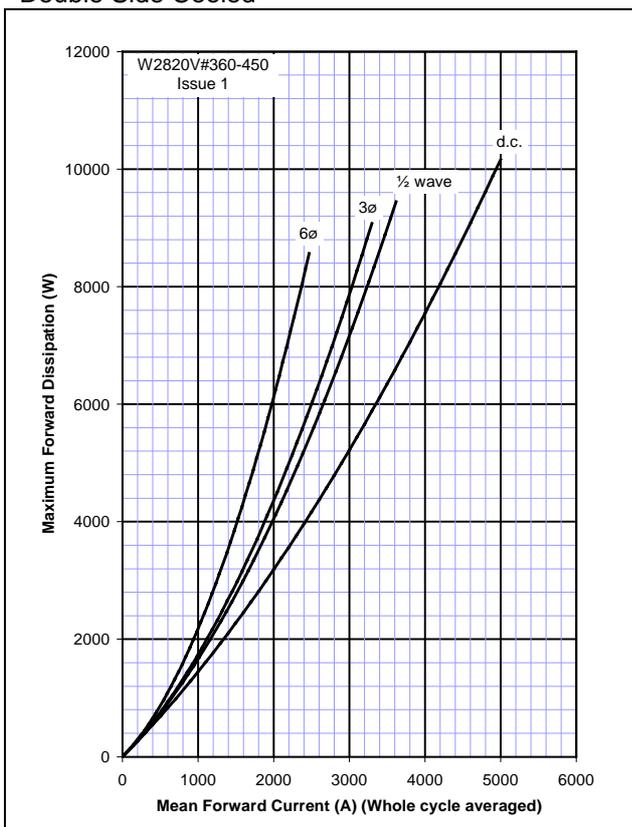


Figure 9 – Forward current vs. Heatsink temperature - Double Side Cooled

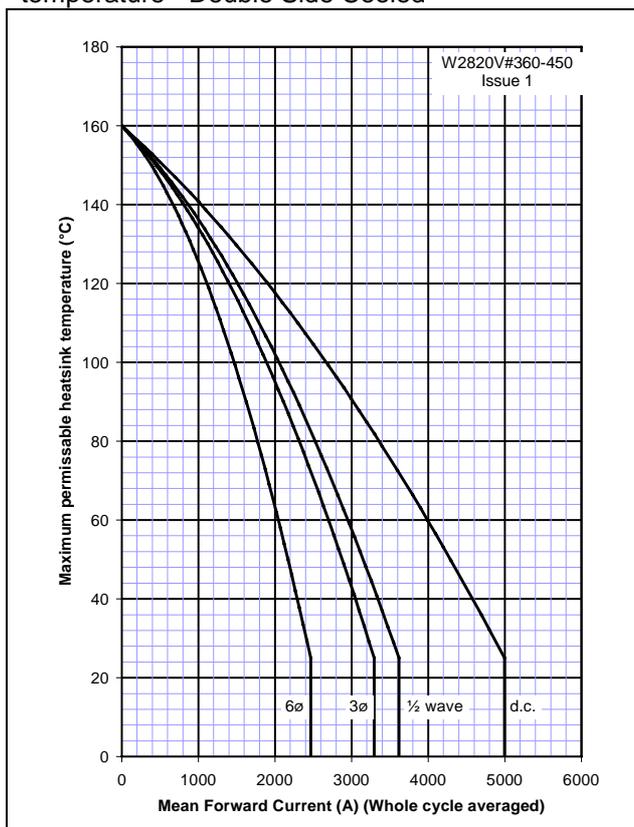


Figure 10 – Forward current vs. Power dissipation – Single Side Cooled

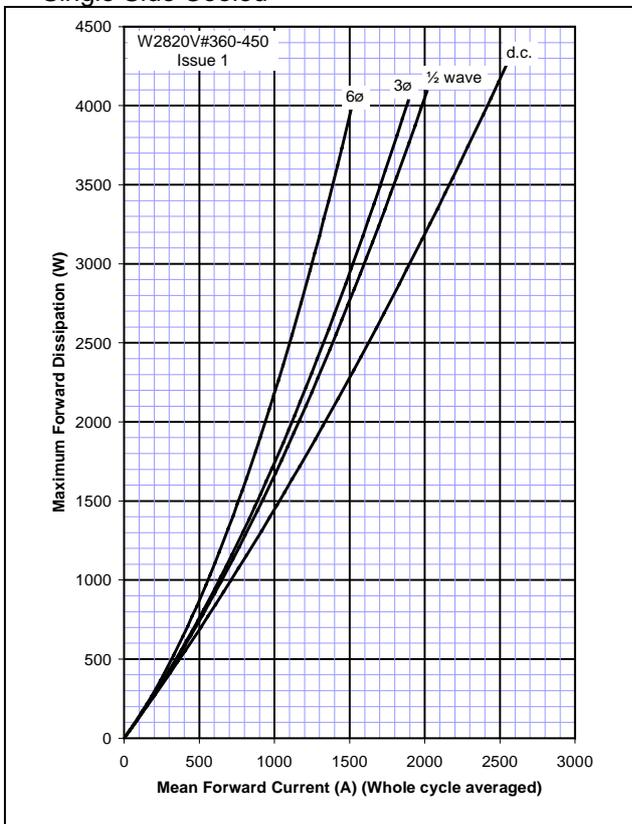
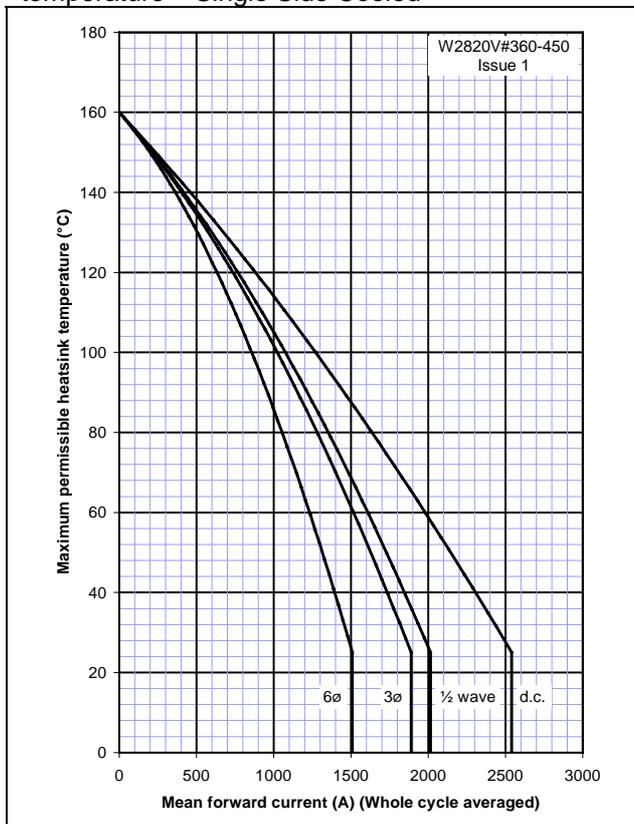
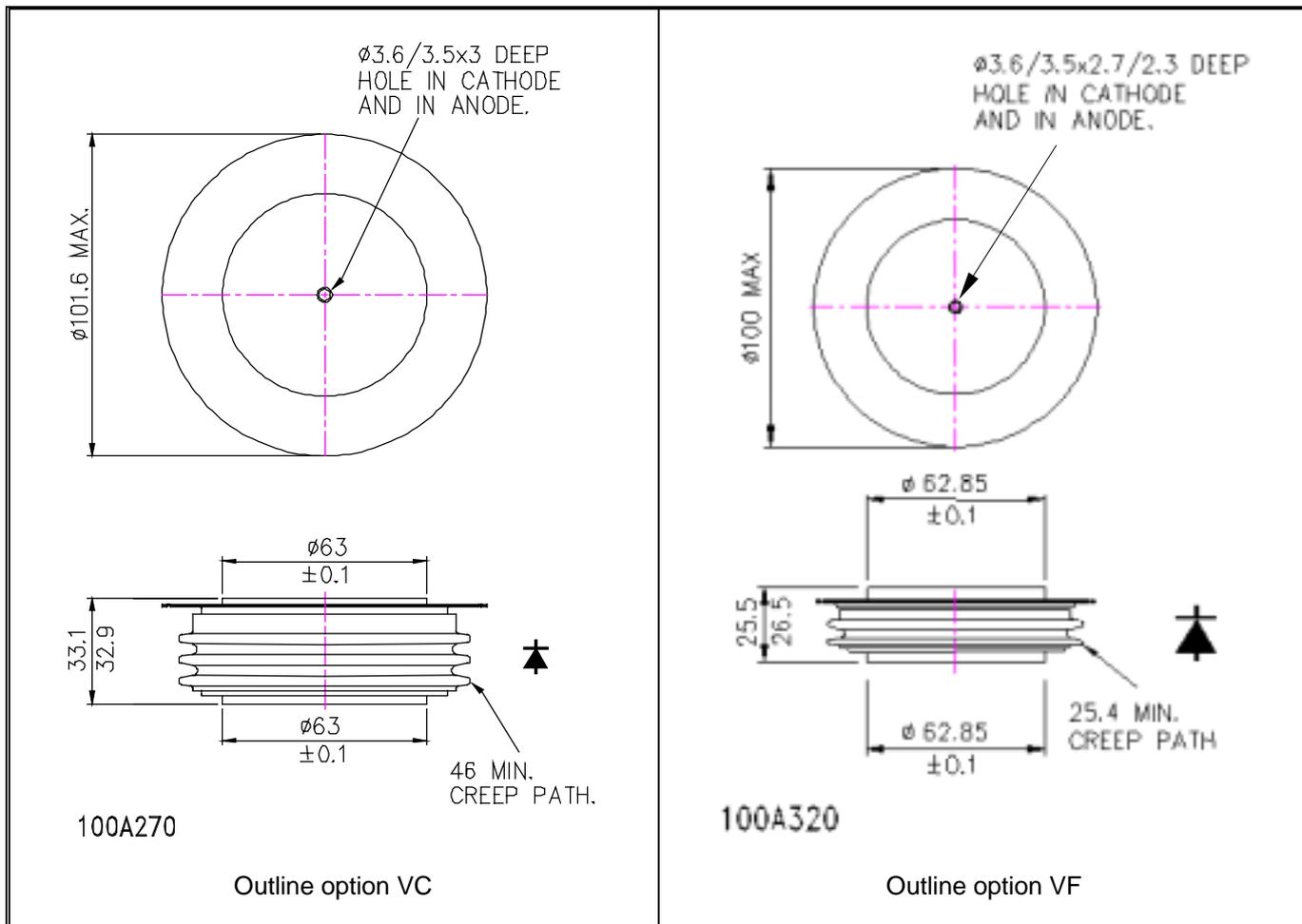


Figure 11 – Forward current vs. Heatsink temperature – Single Side Cooled



Outline Drawing & Ordering Information



ORDERING INFORMATION

(Please quote 10 digit code as below)

W2820	V#	◆◆	0
Fixed Type Code	Fixed outline code VC = 33.1mm clamp height capsule VF = 25.5mm clamp height capsule	Voltage code $V_{RRM}/100$ 36-45	Fixed turn-off time code

Order code: W2820VC380 – 3800V V_{RRM} , 33.1mm clamp height capsule.

IXYS Semiconductor GmbH
Edisonstraße 15
D-68623 Lampertheim
Tel: +49 6206 503-0
Fax: +49 6206 503-627
E-mail: marcom@ixys.de

WESTCODE
An IXYS Company

Westcode Semiconductors Ltd
Langley Park Way, Langley Park,
Chippenham, Wiltshire, SN15 1GE.
Tel: +44 (0)1249 444524
Fax: +44 (0)1249 659448
E-mail: WSL.sales@westcode.com

IXYS Corporation
3540 Bassett Street
Santa Clara CA 95054 USA
Tel: +1 (408) 982 0700
Fax: +1 (408) 496 0670
E-mail: sales@ixys.net

www.westcode.com

www.ixys.com

Westcode Semiconductors Inc
3270 Cherry Avenue
Long Beach CA 90807 USA
Tel: +1 (562) 595 6971
Fax: +1 (562) 595 8182
E-mail: WSI.sales@westcode.com

The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors Westcode Semiconductors Ltd.

© Westcode Semiconductors Ltd.

In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice.

Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.