

## 3-Level TNPC IGBT-Module

### SEMIX405TMLI12E4B

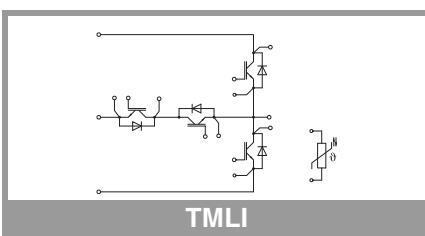
#### Features

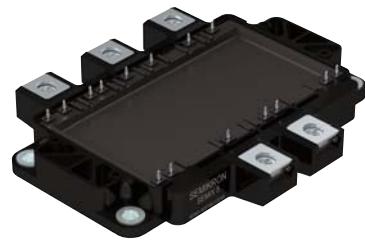
- Solderless assembling solution with PressFIT signal pins and screw power terminals
- IGBT 4 Trench Gate Technology
- $V_{CE(sat)}$  with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

#### Remarks\*

- Case temperature limited to  $T_C=125^\circ\text{C}$  max
- Product reliability results are valid for  $T_{jop}=150^\circ\text{C}$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- For storage and case temperature with TIM see document "TP(HALA P8) SEMIX 5p"

Absolute Maximum Ratings				
Symbol	Conditions	Values	Unit	
<b>IGBT1</b>				
$V_{CES}$	$T_j = 25^\circ\text{C}$	1200	V	
$I_c$	$T_j = 175^\circ\text{C}$	636	A	
		490	A	
$I_{Cnom}$		400	A	
$I_{CRM}$	$I_{CRM} = 3 \times I_{Cnom}$	1200	A	
$V_{GES}$		-20 ... 20	V	
$t_{psc}$	$V_{CC} = 800\text{ V}, V_{GE} \leq 15\text{ V}, T_j = 150^\circ\text{C}, V_{CES} \leq 1200\text{ V}$	10	\mu\text{s}	
$T_j$		-40 ... 175	°C	
<b>IGBT2</b>				
$V_{CES}$	$T_j = 25^\circ\text{C}$	650	V	
$I_c$	$T_j = 175^\circ\text{C}$	453	A	
		340	A	
$I_{Cnom}$		400	A	
$I_{CRM}$	$I_{CRM} = 3 \times I_{Cnom}$	1200	A	
$V_{GES}$		-20 ... 20	V	
$t_{psc}$	$V_{CC} = 360\text{ V}, V_{GE} \leq 15\text{ V}, T_j = 150^\circ\text{C}, V_{CES} \leq 650\text{ V}$	10	\mu\text{s}	
$T_j$		-40 ... 175	°C	
<b>Diode1</b>				
$V_{RRM}$	$T_j = 25^\circ\text{C}$	1200	V	
$I_F$	$T_j = 175^\circ\text{C}$	461	A	
		345	A	
$I_{Fnom}$		400	A	
$I_{FRM}$	$I_{FRM} = 2 \times I_{Fnom}$	800	A	
$I_{FSM}$	$10\text{ ms, sin }180^\circ, T_j = 25^\circ\text{C}$	1980	A	
$T_j$		-40 ... 175	°C	
<b>Diode2</b>				
$V_{RRM}$	$T_j = 25^\circ\text{C}$	650	V	
$I_F$	$T_j = 175^\circ\text{C}$	466	A	
		338	A	
$I_{Fnom}$		400	A	
$I_{FRM}$	$I_{FRM} = 2 \times I_{Fnom}$	800	A	
$I_{FSM}$	$10\text{ ms, sin }180^\circ, T_j = 25^\circ\text{C}$	2646	A	
$T_j$		-40 ... 175	°C	
<b>Module</b>				
$I_{t(RMS)}$		450	A	
$T_{stg}$	module without TIM	-40 ... 125	°C	
$V_{isol}$	AC sinus 50Hz, t = 1 min	4000	V	





**SEMIX® 5**

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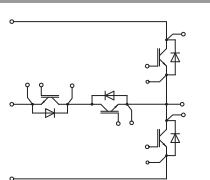
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- IGBT 4 Trench Gate Technology
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Characteristics		Conditions	min.	typ.	max.	Unit
Symbol						
<b>IGBT1</b>						
$V_{CE(sat)}$	$I_C = 400 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		1.80	2.05	V
$V_{CE0}$	chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		2.20	2.40	V
$r_{CE}$	$V_{GE} = 15 \text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		2.5	2.9	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 15.2 \text{ mA}$		5	5.8	6.5	V
$I_{CES}$	$V_{GE} = 0 \text{ V}, V_{CE} = 1200 \text{ V}, T_j = 25^\circ\text{C}$				5	mA
$C_{ies}$	$V_{CE} = 25 \text{ V}$	$f = 1 \text{ MHz}$		24.6		nF
$C_{oes}$	$V_{GE} = 0 \text{ V}$	$f = 1 \text{ MHz}$		1.62		nF
$C_{res}$		$f = 1 \text{ MHz}$		1.38		nF
$Q_G$	$V_{GE} = -15 \text{ V}...+15 \text{ V}$			3026		nC
$R_{Gint}$	$T_j = 25^\circ\text{C}$			1.9		$\Omega$
$t_{d(on)}$	$V_{CC} = 300 \text{ V}$	$T_j = 150^\circ\text{C}$		232		ns
$t_r$	$I_C = 400 \text{ A}$	$T_j = 150^\circ\text{C}$		128		ns
$E_{on}$	$V_{GE} = +15/-15 \text{ V}$	$R_{G\,on} = 1.5 \Omega$		6		mJ
$t_{d(off)}$	$R_{G\,off} = 1.5 \Omega$	$T_j = 150^\circ\text{C}$		422		ns
$t_f$	$di/dt_{on} = 2437 \text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$		121		ns
	$di/dt_{off} = 3000 \text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$			28	mJ
$E_{off}$						
$R_{th(j-c)}$	per IGBT				0.068	K/W
$R_{th(c-s)}$	per IGBT ( $\lambda_{grease}=0.81 \text{ W}/(\text{m}^*\text{K})$ )			0.027		K/W
$R_{th(c-s)}$	per IGBT, pre-applied phase change material			0.015		K/W
<b>IGBT2</b>						
$V_{CE(sat)}$	$I_C = 400 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		1.55	1.95	V
$V_{CE0}$	chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		1.75	2.15	V
$r_{CE}$	$V_{GE} = 15 \text{ V}$ chiplevel	$T_j = 25^\circ\text{C}$ $T_j = 150^\circ\text{C}$		1.63	2.4	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 8 \text{ mA}$		5.1	5.8	6.4	V
$I_{CES}$	$V_{GE} = 0 \text{ V}, V_{CE} = 650 \text{ V}, T_j = 25^\circ\text{C}$				0.7	mA
$C_{ies}$	$V_{CE} = 25 \text{ V}$	$f = 1 \text{ MHz}$		24.7		nF
$C_{oes}$	$V_{GE} = 0 \text{ V}$	$f = 1 \text{ MHz}$		1.54		nF
$C_{res}$		$f = 1 \text{ MHz}$		0.73		nF
$Q_G$	$V_{GE} = -15 \text{ V}...+15 \text{ V}$			4420		nC
$R_{Gint}$	$T_j = 25^\circ\text{C}$			1.0		$\Omega$
$t_{d(on)}$	$V_{CC} = 300 \text{ V}$	$T_j = 150^\circ\text{C}$		170		ns
$t_r$	$I_C = 400 \text{ A}$	$T_j = 150^\circ\text{C}$		118		ns
$E_{on}$	$V_{GE} = +15/-15 \text{ V}$	$R_{G\,on} = 1.5 \Omega$		3		mJ
$t_{d(off)}$	$R_{G\,off} = 1.5 \Omega$	$T_j = 150^\circ\text{C}$		380		ns
$t_f$	$di/dt_{on} = 3100 \text{ A}/\mu\text{s}$	$T_j = 150^\circ\text{C}$		127		ns
$E_{off}$		$T_j = 150^\circ\text{C}$		23		mJ
$R_{th(j-c)}$	per IGBT				0.14	K/W
$R_{th(c-s)}$	per IGBT ( $\lambda_{grease}=0.81 \text{ W}/(\text{m}^*\text{K})$ )			0.058		K/W
$R_{th(c-s)}$	per IGBT, pre-applied phase change material			0.046		K/W



**TMLI**



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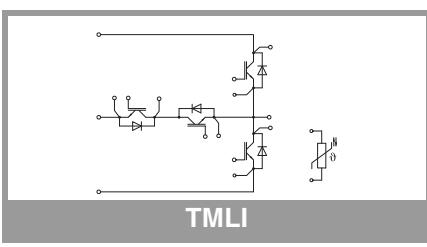
#### Features

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- IGBT 4 Trench Gate Technology
- $V_{CE(sat)}$  with positive temperature coefficient
- Low inductance case
- Reliable mechanical design with injection moulded terminals and reliable internal connections
- UL recognized file no. E63532
- NTC temperature sensor inside

#### Remarks\*

- Case temperature limited to  $T_C=125^\circ\text{C}$  max
- Product reliability results are valid for  $T_{jop}=150^\circ\text{C}$
- IGBT1: outer IGBTs T1 & T4
- IGBT2: inner IGBTs T2 & T3
- Diode1: outer diodes D1 & D4
- Diode2: inner diodes D2 & D3
- For storage and case temperature with TIM see document "TP(HALA P8) SEMIX 5p"

Characteristics		Symbol	Conditions	min.	typ.	max.	Unit						
Diode1													
<b>V<sub>F</sub> = V<sub>EC</sub></b>													
$I_F = 400 \text{ A}$	$T_j = 25^\circ\text{C}$			2.20	2.52		V						
	$V_{GE} = 0 \text{ V}$ chiplevel			2.15	2.47		V						
<b>V<sub>F0</sub></b>													
	$T_j = 25^\circ\text{C}$			1.30	1.50		V						
	$T_j = 150^\circ\text{C}$			0.90	1.10		V						
<b>r<sub>F</sub></b>													
	$T_j = 25^\circ\text{C}$			2.3	2.6		mΩ						
	$T_j = 150^\circ\text{C}$			3.1	3.4		mΩ						
<b>I<sub>RRM</sub></b>													
$I_F = 400 \text{ A}$	$T_j = 150^\circ\text{C}$			213			A						
	$\text{di/dt}_{\text{off}} = 3100 \text{ A}/\mu\text{s}$				55.5		μC						
<b>Q<sub>rr</sub></b>													
$V_{CC} = 300 \text{ V}$	$T_j = 150^\circ\text{C}$												
	$V_{GE} = +15/-15 \text{ V}$												
<b>E<sub>rr</sub></b>													
	$T_j = 150^\circ\text{C}$			11.8			mJ						
<b>R<sub>th(j-c)</sub></b>													
per diode					0.13		K/W						
<b>R<sub>th(c-s)</sub></b>													
per diode ( $\lambda_{\text{grease}}=0.81 \text{ W}/(\text{m}^*\text{K})$ )				0.036			K/W						
<b>R<sub>th(c-s)</sub></b>													
per diode, pre-applied phase change material				0.028			K/W						
<b>Diode2</b>													
<b>V<sub>F</sub> = V<sub>EC</sub></b>													
$I_F = 400 \text{ A}$	$T_j = 25^\circ\text{C}$			1.39	1.75		V						
	$V_{GE} = 0 \text{ V}$ chiplevel			1.38	1.76		V						
<b>V<sub>F0</sub></b>													
	$T_j = 25^\circ\text{C}$			1.04	1.24		V						
	$T_j = 150^\circ\text{C}$			0.85	0.99		V						
<b>r<sub>F</sub></b>													
	$T_j = 25^\circ\text{C}$			0.88	1.30		mΩ						
	$T_j = 150^\circ\text{C}$			1.32	1.93		mΩ						
<b>I<sub>RRM</sub></b>													
$I_F = 400 \text{ A}$	$T_j = 150^\circ\text{C}$			242			A						
	$\text{di/dt}_{\text{off}} = 2437 \text{ A}/\mu\text{s}$				46		μC						
<b>Q<sub>rr</sub></b>													
$V_R = 300 \text{ V}$	$T_j = 150^\circ\text{C}$			11			mJ						
	$V_{GE} = +15/-15 \text{ V}$												
<b>E<sub>rr</sub></b>													
per diode				0.18			K/W						
<b>R<sub>th(j-c)</sub></b>													
per diode ( $\lambda_{\text{grease}}=0.81 \text{ W}/(\text{m}^*\text{K})$ )				0.053			K/W						
<b>R<sub>th(c-s)</sub></b>													
per diode, pre-applied phase change material				0.046			K/W						
<b>Module</b>													
<b>L<sub>SCE1</sub></b>													
<b>L<sub>CE</sub></b>													
<b>R<sub>CC+EE'</sub></b>													
	measured between terminal 5 and 1	$T_C = 25^\circ\text{C}$		0.8			mΩ						
		$T_C = 125^\circ\text{C}$		1.1			mΩ						
<b>R<sub>th(c-s)1</sub></b>													
calculated without thermal coupling				0.005			K/W						
<b>R<sub>th(c-s)2</sub></b>													
including thermal coupling, Ts underneath module ( $\lambda_{\text{grease}}=0.81 \text{ W}/(\text{m}^*\text{K})$ )				0.0081			K/W						
<b>R<sub>th(c-s)2</sub></b>													
including thermal coupling, Ts underneath module, pre-applied phase change material				0.0055			K/W						
<b>M<sub>s</sub></b>													
to heat sink (M5)				3	6		Nm						
<b>M<sub>t</sub></b>													
to terminals (M6)				3	6		Nm						
<b>W</b>													
<b>Temperature Sensor</b>													
<b>R<sub>100</sub></b>													
$T_c=100^\circ\text{C}$ ( $R_{25}=5 \text{ k}\Omega$ )				493 ± 5%			Ω						
<b>B<sub>100/125</sub></b>													
$R_{(T)}=R_{100}\exp[B_{100/125}(1/T-1/T_{100})]$ ; $T[\text{K}]$ ;				3550 ±2%			K						



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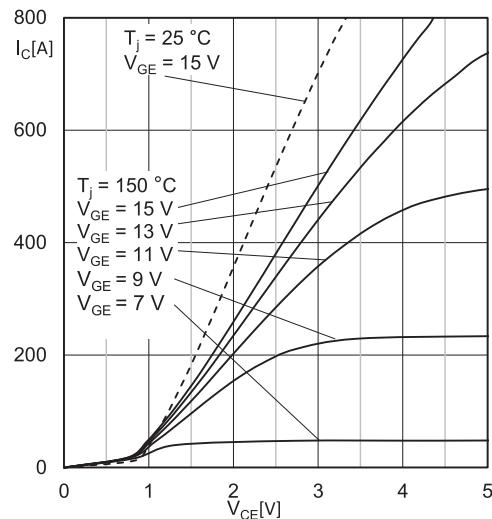


Fig. 1: Typ. IGBT1 output characteristic, incl.  $R_{CC} + EE'$

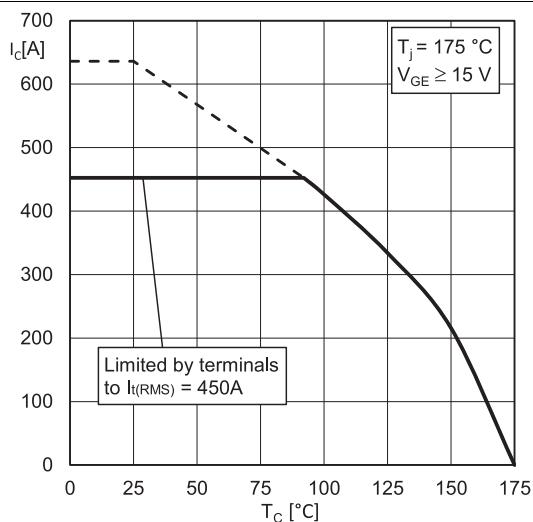


Fig. 2: IGBT1 rated current vs. Temperature  $I_c=f(T_c)$

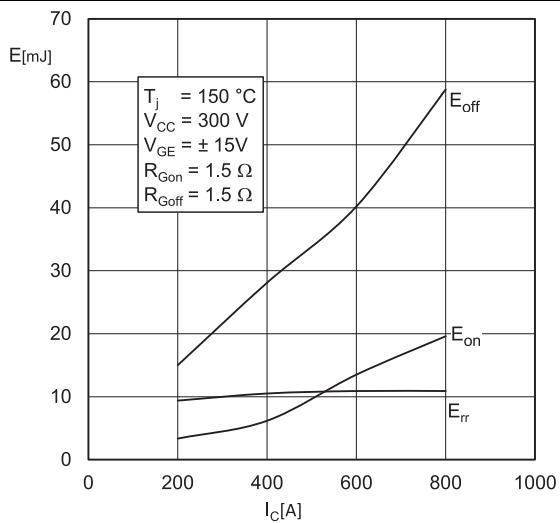


Fig. 3: Typ. IGBT1 & Diode2 turn-on /-off energy =  $f(I_c)$

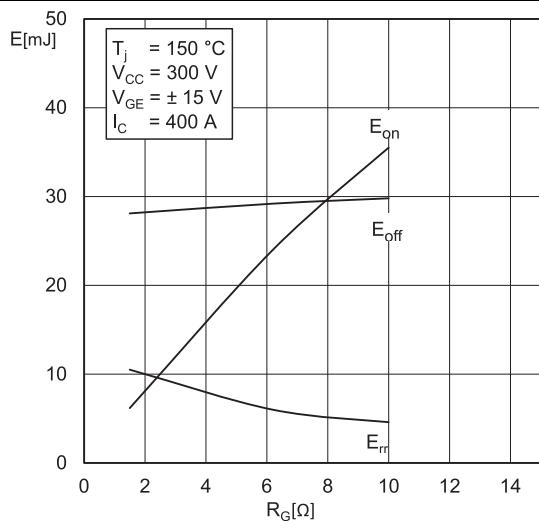


Fig. 4: Typ. IGBT1 & Diode2 turn-on /-off energy =  $f(R_G)$

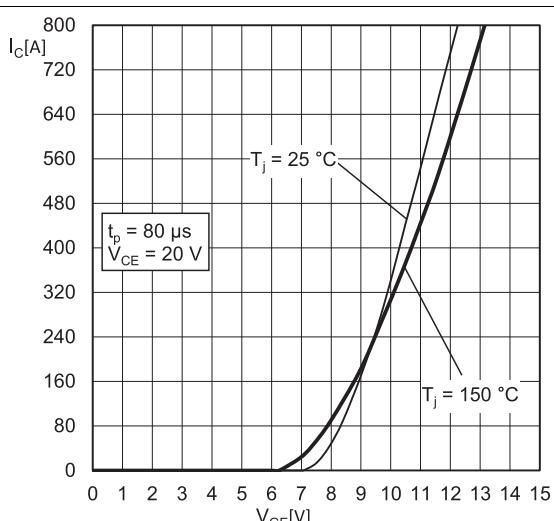


Fig. 5: Typ. IGBT1 transfer characteristic

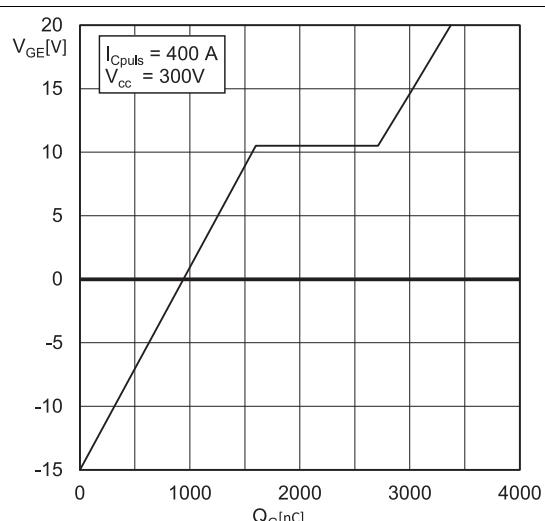


Fig. 6: Typ. IGBT1 gate charge characteristic

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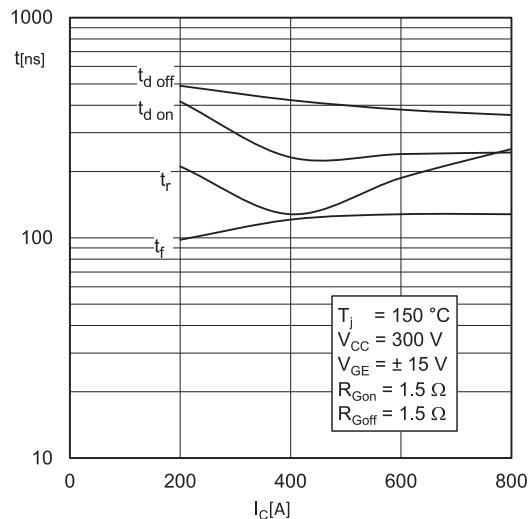


Fig. 7: Typ. IGBT1 switching times vs.  $I_C$

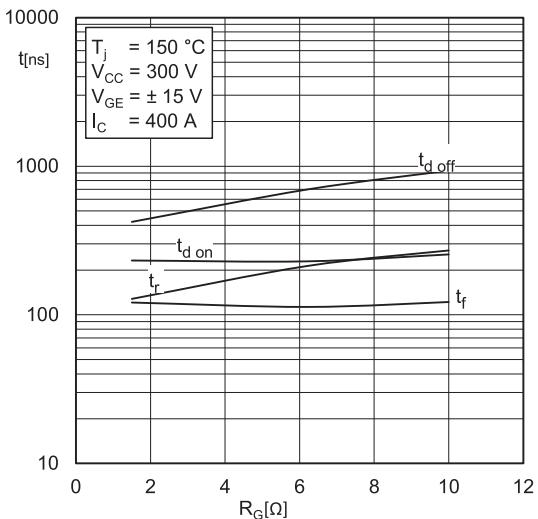


Fig. 8: Typ. IGBT1 switching times vs. gate resistor  $R_G$

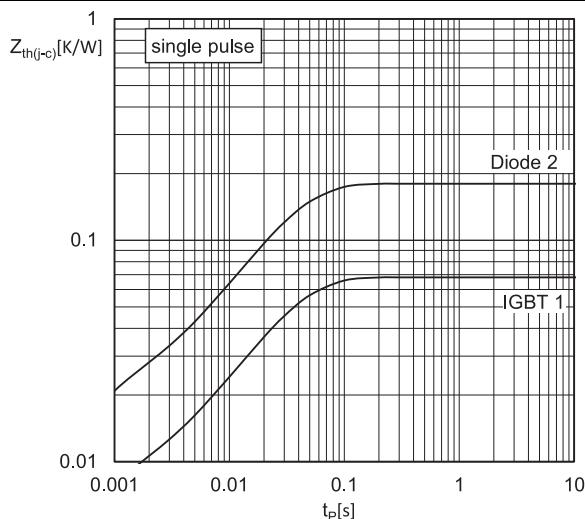


Fig. 9: Transient thermal impedance of IGBT1 & Diode2

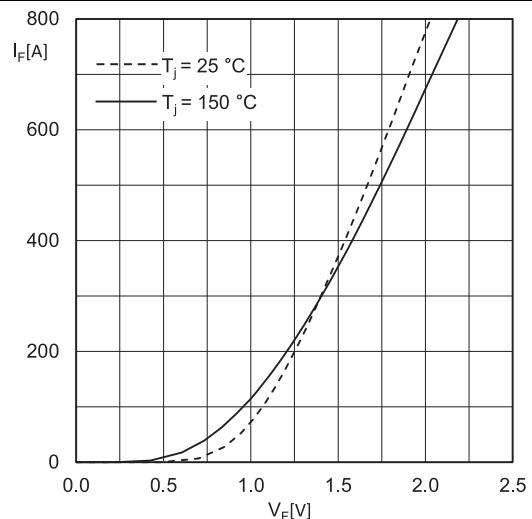


Fig. 10: Typ. Diode2 forward characteristic, incl.  $R_{CC} + EE'$

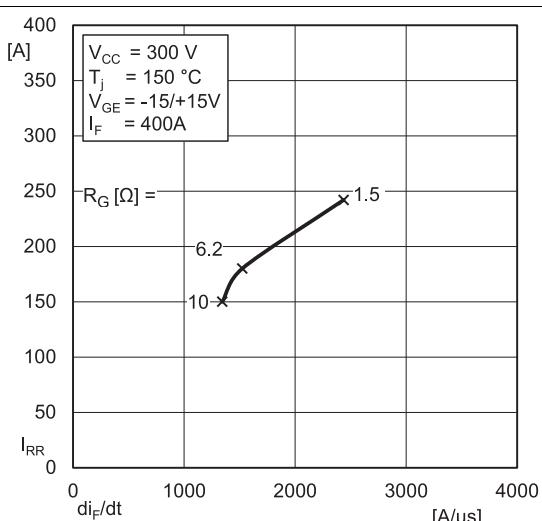


Fig. 11: Typ. Diode2 peak reverse recovery current

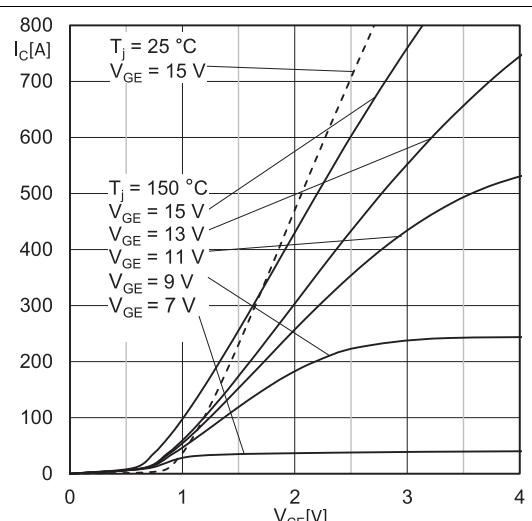


Fig. 13: Typ. IGBT2 output characteristic, incl.  $R_{CC} + EE'$

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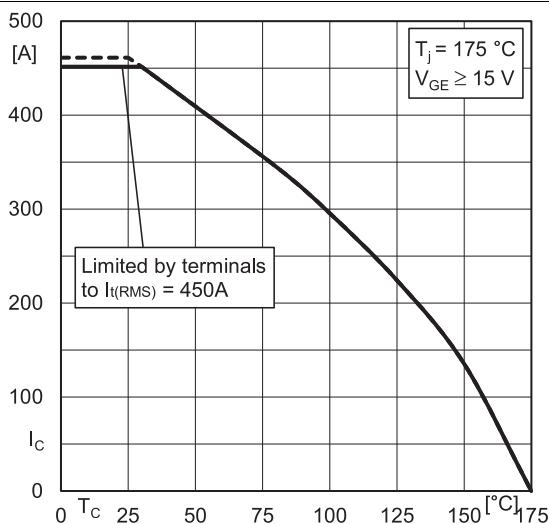


Fig. 14: IGBT2 Rated current vs. Temperature  $I_c = f(T_c)$

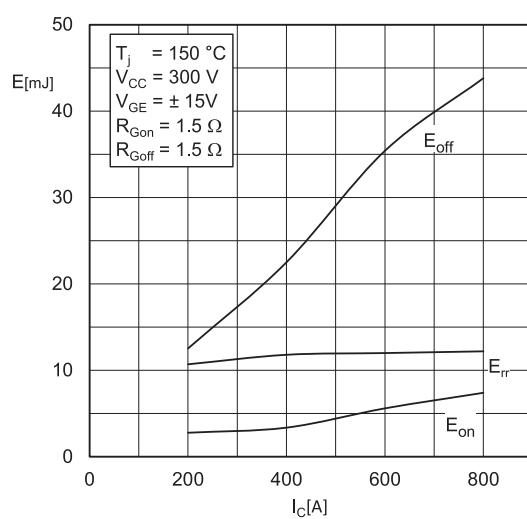


Fig. 15: Typ. IGBT2 & Diode1 turn-on / -off energy = f( $I_c$ )

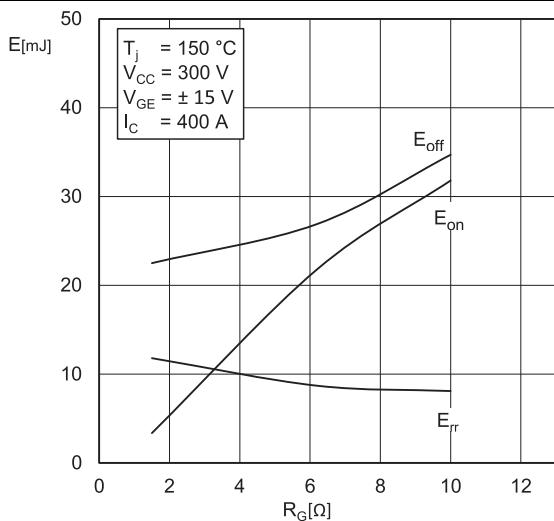


Fig. 16: Typ. IGBT2 & Diode1 turn-on / -off energy = f( $R_G$ )

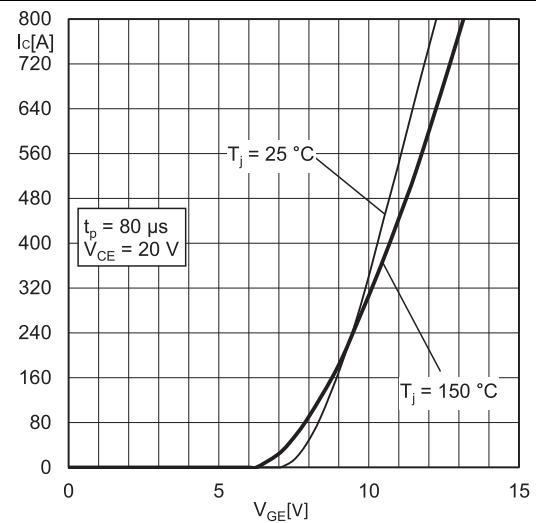


Fig. 17: Typ. IGBT2 transfer characteristic

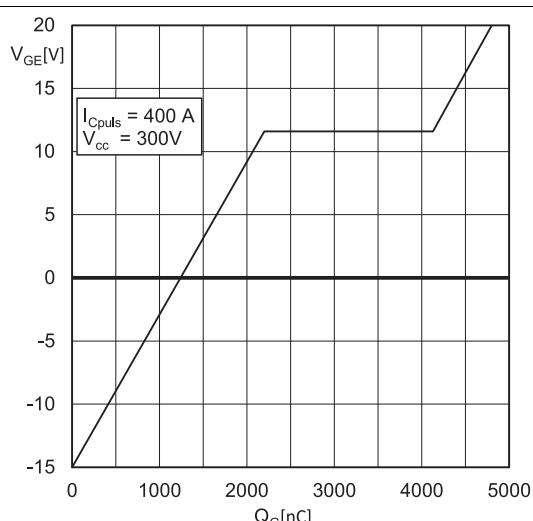


Fig. 18: Typ. IGBT2 gate charge characteristic

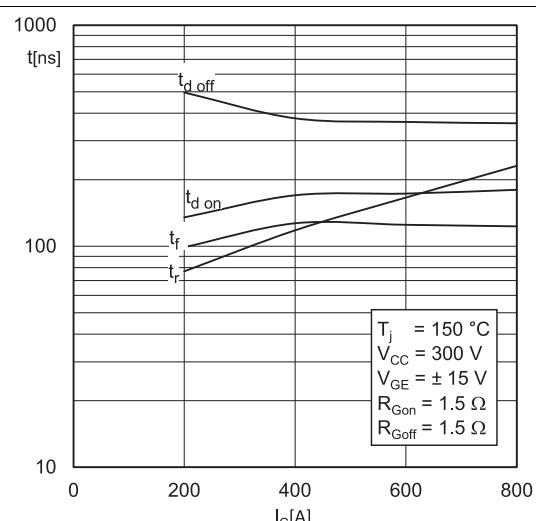


Fig. 19: Typ. IGBT2 switching times vs.  $I_c$

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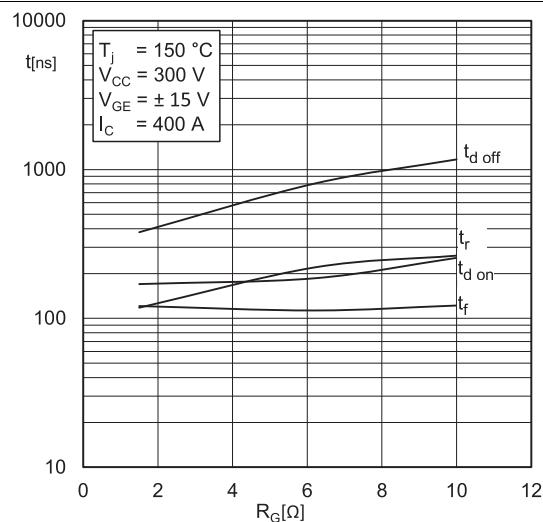


Fig. 20: Typ. IGBT2 switching times vs. gate resistor  $R_G$

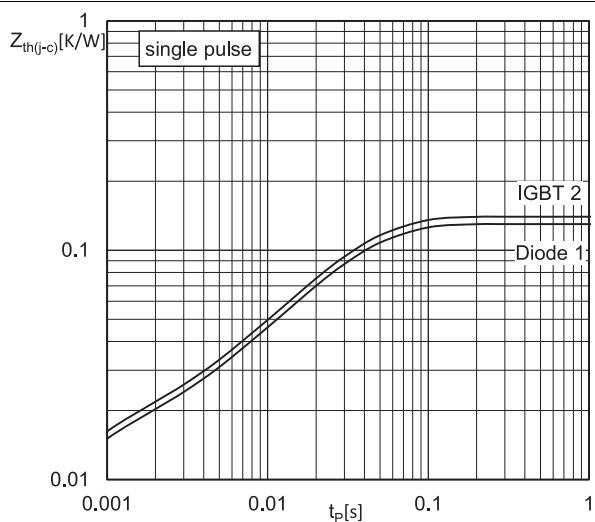


Fig. 21: Transient thermal impedance of IGBT2 & Diode1

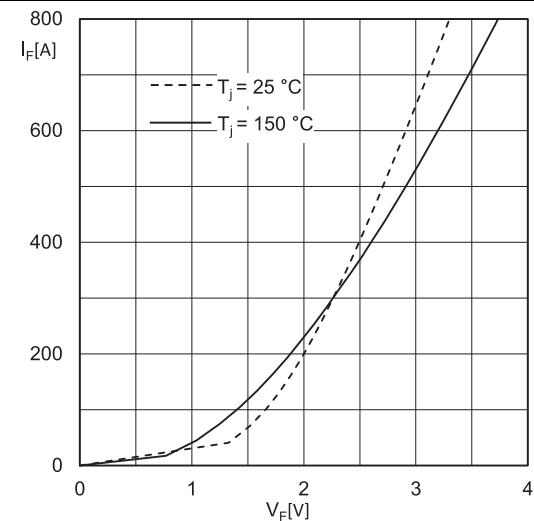


Fig. 22: Typ. Diode1 forward characteristic, incl.  $R_{CC' + EE'}$

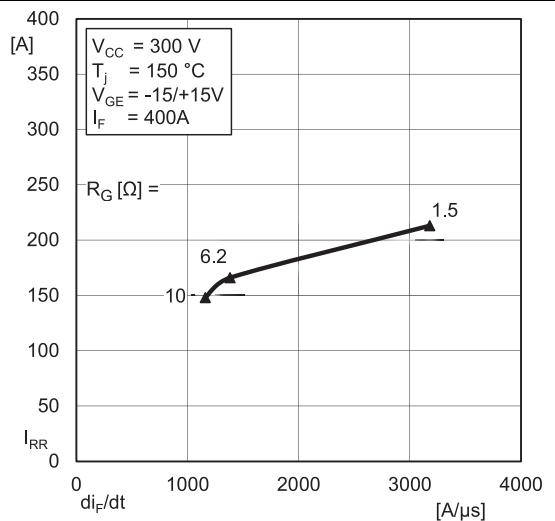
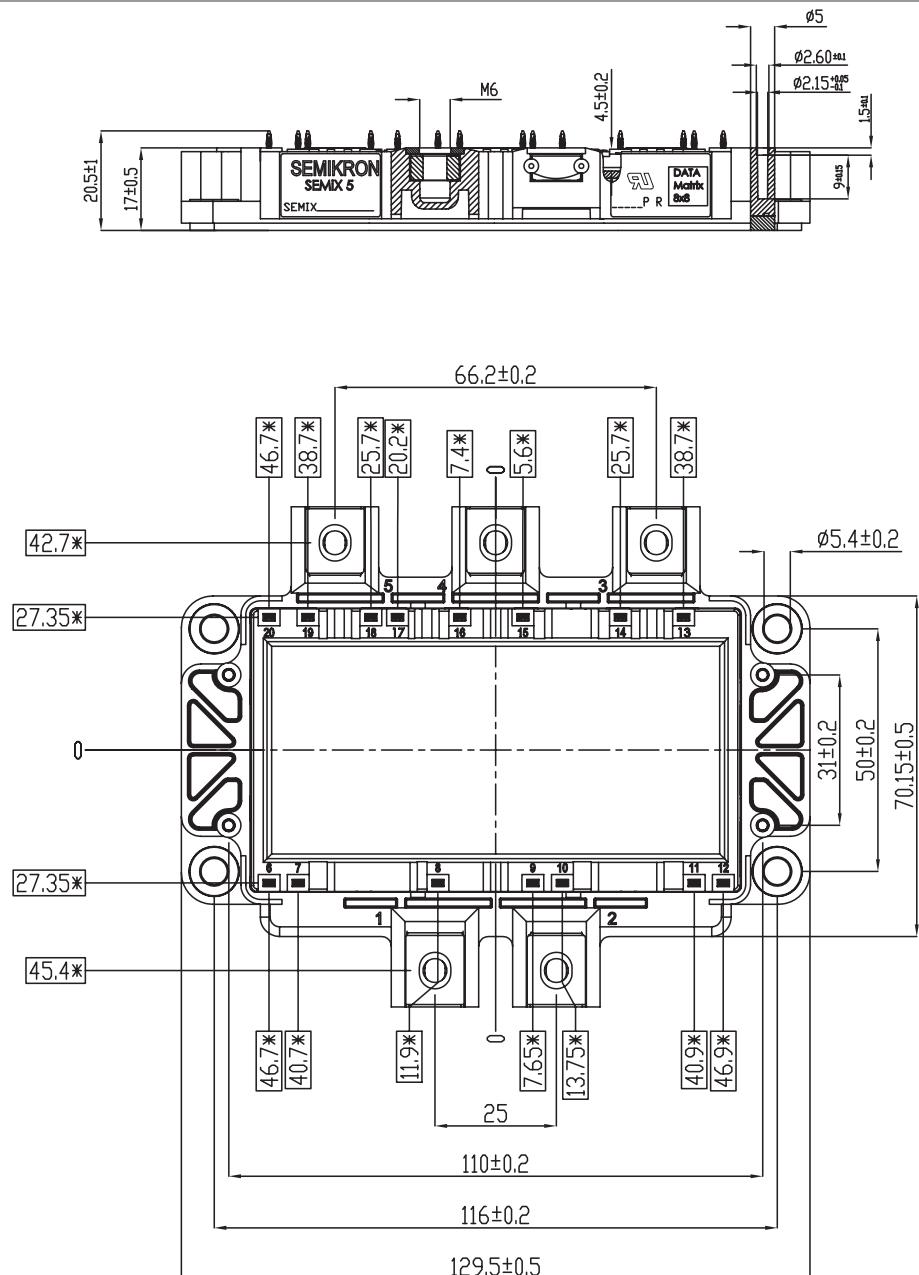


Fig. 23: Typ. Diode1 peak reverse recovery current

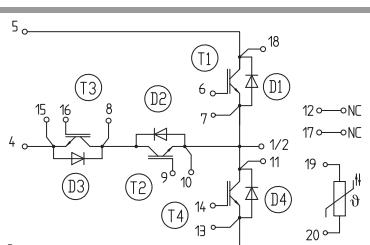
# SEMiX405TMLI12E4B



\* = All dimension with tolerance of  $\pm 0.4$

For technical details please refer  
to SEMiX(R)5 Mounting Instruction

SEMiX5p



TMLI

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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