

## 6MBI75VA-120-50

**IGBT Modules** 

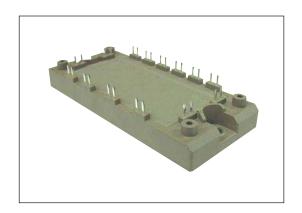
# IGBT MODULE (V series) 1200V / 75A / 6 in one package

#### **■** Features

Compact Package P.C.Board Mount Low Vce (sat)

#### ■ Applications

Inverter for Motor Drive
AC and DC Servo Drive Amplifier
Uninterruptible Power Supply
Industrial machines, such as welding machines



#### ■ Maximum Ratings and Characteristics

#### ■ Maximum ratings (at Tc=25°C unless otherwise specified)

Items			Symbols	Conditions		Maximum ratings	Units	
	Collector-Emitter voltage		V <sub>CES</sub>			1200	V	
Inverter	Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
	Collector current		Ic	Continuous	Tc=100°C	75		
			Icp	1ms	Tc=80°C	150	^	
			-lc			75	Α	
			-lc pulse	1ms		150		
	Collector power dissipation		Pc	1 device		385	W	
Junction temperature			Tj			175		
Operating junciton temperature (under switching conditions)			Tjop			150	°C	
Case temperature			Tc			125		
Storage temperature			Tstg			-40 to +125		
Iso	lation voltage	between terminal and copper base (*1) between thermistor and others (*2)	Viso	AC : 1min.		2500	VAC	
Sc	rew torque	torque Mounting (*3) -		M5		3.5	N m	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value: 2.5-3.5 Nm (M5)

#### ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

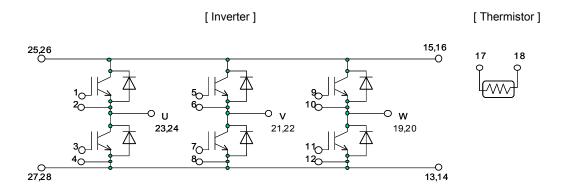
Items		Symbols	Conditions		Characteristics			Units
		Syllibols			min.	typ.	max.	Ullits
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V		-	-	1.0	mA
	Gate-Emitter leakage current	te-Emitter leakage current IGES VGE = 0V, VGE = ±20V		-	-	200	nA	
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 75mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (terminal)	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	Tj=25°C	-	2.25	2.70	V
				Tj=125°C	-	2.60	-	
				Tj=150°C	-	2.65	-	
		V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 75A	Tj=25°C	-	1.85	2.30	
				Tj=125°C	-	2.20	-	
				Tj=150°C	-	2.25	-	
	Internal gate resistance	R <sub>g</sub> (int)	-		-	10	-	Ω
ē	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	6.0	-	nF
Inverter		ton	Vcc = 600V		-	0.39	1.20	μs
≦	Turn-on time	tr			-	0.09	0.60	
		tr (i)	Ic = 75A -V <sub>GE</sub> = +15 / -15V	-	0.03	-		
	Turn-off time	toff	$R_G = 2.2\Omega$	-	0.53	1.00		
	Turn-on time	tf			-	0.06	0.30	
	Forward on voltage	V <sub>F</sub> (terminal)	I <sub>F</sub> = 75A	Tj=25°C	-	2.10	2.55	V
				Tj=125°C	-	2.25	-	
				Tj=150°C	-	2.20	-	
		V <sub>F</sub> (chip)	I <sub>F</sub> = 75A	Tj=25°C	-	1.70	2.15	
				Tj=125°C	-	1.85	-	
				Tj=150°C	-	1.80	-	
	Reverse recovery time	trr	I <sub>F</sub> = 75A		-	-	0.35	μs
ţo	Resistance	R	T = 25°C		-	5000	-	Ω
Thermistor	Resistance		T = 100°C		465	495	520	
The The	B value	<b>value</b> B T = 25 / 50°C			3305	3375	3450	K

#### ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items			min.	typ.	max.	Ullits
Thermal resistance (1device)	Dth(i o)	Inverter IGBT	-	-	0.39	°C/W
Thermal resistance (Tuevice)	Rth(j-c)	Inverter FWD	-	-	0.55	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.05	-	

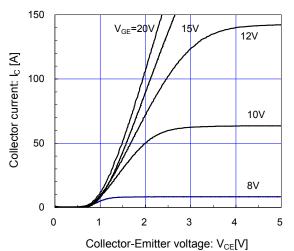
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### **■** Equivalent Circuit Schematic

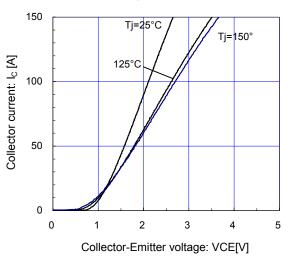


#### **■** Characteristics (Representative)

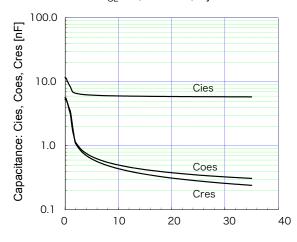
 $\label{eq:continuous} \begin{tabular}{ll} \mbox{ Inverter } \mbox{ } \mbo$ 



 $[Inverter\ ] \\ Collector\ current\ vs.\ Collector-Emitter\ voltage\ (typ.) \\ V_{GE} = 15V\ /\ chip$ 

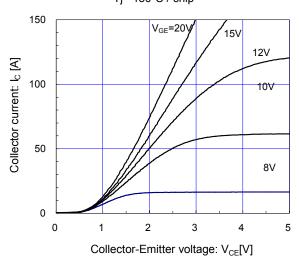


 $[Inverter\ ] \\ Capacitance\ vs.\ Collector-Emitter\ voltage\ (typ.) \\ V_{GE}=0V,\ f=1MHz,\ Tj=25^{\circ}C$ 

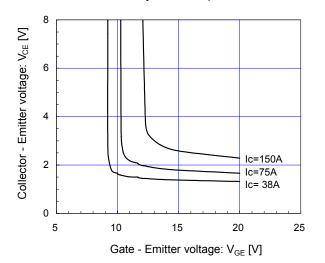


Collector - Emitter voltage: V<sub>CE</sub> [V]

 $\label{eq:continuous} \begin{tabular}{ll} [Inverter ] \\ Collector current vs. Collector-Emitter voltage (typ.) \\ Tj= 150 {\rm ^oC}\ /\ chip \end{tabular}$ 



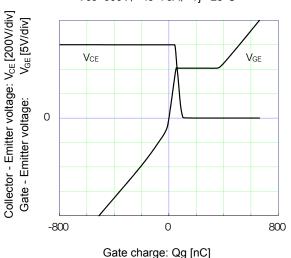
 $\label{eq:continuous} \begin{tabular}{ll} \mbox{ Inverter ]} \\ \mbox{ Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)} \\ \mbox{ Tj= } 25^{\circ}\mbox{C / chip} \\ \end{tabular}$ 



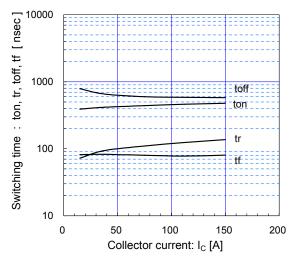
[ Inverter ]

Dynamic gate charge (typ.)

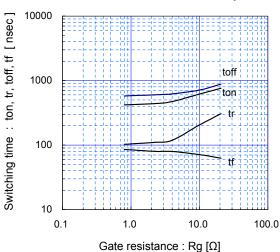
Vcc=600V, Ic=75A, Tj= 25°C



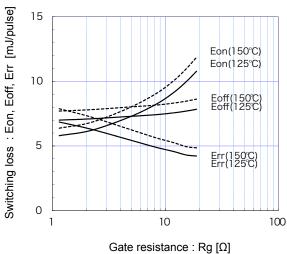
[ Inverter ]
Switching time vs. Collector current (typ.)
Vcc=600V, VGE=±15V, Rg=2.2Ω, Tj= 125°C



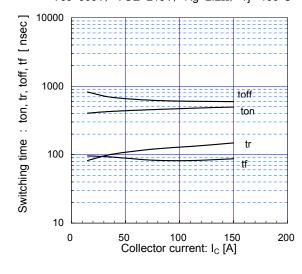
[Inverter]
Switching time vs. gate resistance (typ.)
Vcc=600V, Ic=75A, VGE=±15V, Tj= 125°C



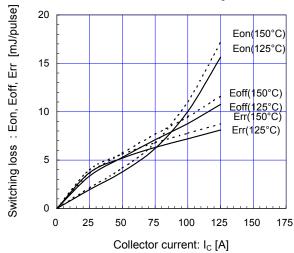
[Inverter]
Switching loss vs. gate resistance (typ.)
Vcc=600V, Ic=75A, VGE=±15V



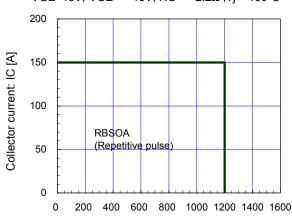
[ Inverter ] Switching time vs. Collector current (typ.) Vcc=600V,  $VGE=\pm15V$ ,  $Rg=2.2\Omega$ ,  $Tj=150^{\circ}C$ 



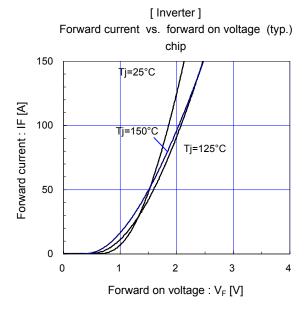
[ Inverter ] Switching loss vs. Collector current (typ.) Vcc=600V, VGE= $\pm$ 15V, Rg=2.2 $\Omega$ 

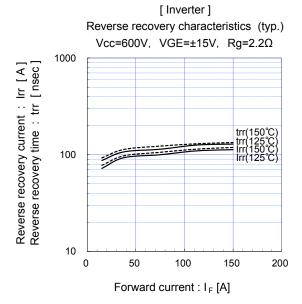


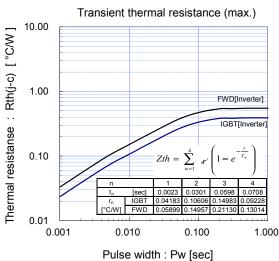
[ Inverter ] Reverse bias safe operating area (max.)  $+VGE=15V, -VGE \le 15V, RG \ge 2.2\Omega, Tj = 150^{\circ}C$ 

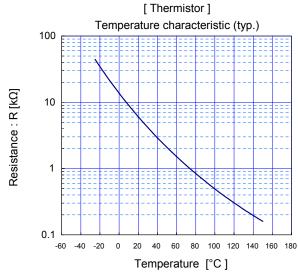


Collector-Emitter voltage : V<sub>CE</sub> [V] (Main terminals)

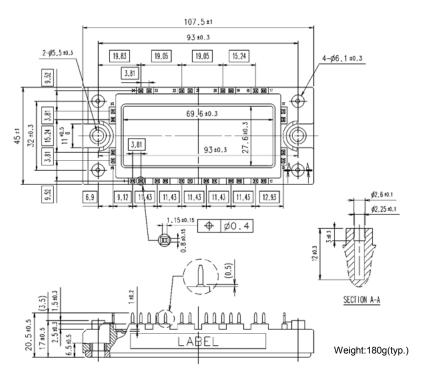








#### ■ Outline Drawings, mm



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