

## 6MBI150VX-120-50

**IGBT Modules** 

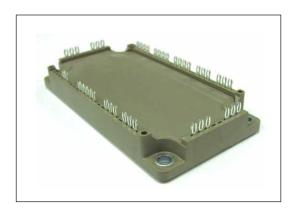
# IGBT MODULE (V series) 1200V / 150A / 6 in one package

#### **■** Features

Compact Package P.C.Board Mount Low Vce (sat)

#### ■ Applications

Inverter for Motor Drive
AC and DC Servo Drive Amplifier
Uninterruptible Power Supply
Industrial machines, such as welding machines



#### ■ Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items			Symbols	Conditions		Maximum ratings	Units	
	Collector-Emitter voltage		Vces			1200	V	
Inverter	Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
	Collector current		Ic	Continuous	Tc=100°C	150		
			Icp	1ms	Tc=80°C	300	^	
			-lc			150	Α	
			-lc pulse	1ms		300		
	Collector power dissipation		Pc	1 device		770	W	
Junction temperature			Tj			175		
Operating junciton temperature (under switching conditions)			Tjop			150	°C	
Case temperature			Tc			125		
Storage temperature			Tstg			-40 to +125		
Isc	lation voltage	between terminal and copper base (*1) between thermistor and others (*2)	Viso	AC : 1min.		2500	VAC	
Sc	Screw torque Mounting (*3)		-	M5		3.5	N m	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value: 2.5-3.5 Nm (M5)

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#### ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

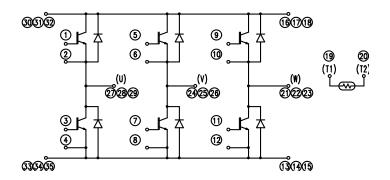
Items		Symbols	Conditions		Characteristics			Units
		Syllibols			min.	typ.	max.	Units
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V		-	-	1.0	mA
	Gate-Emitter leakage current	Emitter leakage current IGES VGE = 0V, VGE = ±20V		-	-	200	nA	
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 150mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (terminal)	V <sub>GE</sub> = 15V I <sub>C</sub> = 150A	Tj=25°C	-	2.40	2.85	V
				Tj=125°C	-	2.70	-	
				Tj=150°C	-	2.75	-	
		V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 150A	Tj=25°C	-	1.75	2.20	
				Tj=125°C	-	2.05	-	
				Tj=150°C	-	2.10	-	
	Internal gate resistance	R <sub>g</sub> (int)	-		-	5.0	-	Ω
ē	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	13.7	-	nF
Inverter		ton	Vcc = 600V		-	0.39	1.20	μs
≦	Turn-on time	tr			-	0.09	0.60	
		tr (i)	Ic = 150A -V <sub>GE</sub> = +15 / -15V	-	0.03	-		
	Town off time	toff	$R_G = 1.1\Omega$	-	0.53	1.00		
	Turn-off time	tf	1		-	0.06	0.30	
	Forward on voltage	V <sub>F</sub> (terminal)	I <sub>F</sub> = 150A	Tj=25°C	-	2.35	2.80	V
				Tj=125°C	-	2.50	-	
				Tj=150°C	-	2.45	-	
		V <sub>F</sub> (chip)	I <sub>F</sub> = 150A	Tj=25°C	-	1.70	2.15	
				Tj=125°C	-	1.85	-	
				Tj=150°C	-	1.80	-	
	Reverse recovery time	trr	I <sub>F</sub> = 150A		-	-	0.35	μs
ţō	Pacietones	R	T = 25°C		-	5000	-	Ω
Thermistor	Resistance		T = 100°C		465	495	520	
重	B value	<b>value</b> B T = 25 / 50°C			3305	3375	3450	K

#### Thermal resistance characteristics

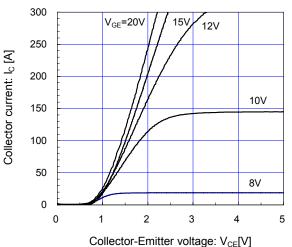
Items	Symbols	Conditions	Characteristics			Units
items			min.	typ.	max.	Ullits
Thermal resistance (1device)	Rth(j-c)	Inverter IGBT	-	-	0.195	°C/W
Thermal resistance (Tuevice)		Inverter FWD	-	-	0.34	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.05	-	

Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

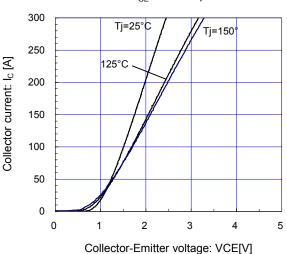
#### **■** Equivalent Circuit Schematic



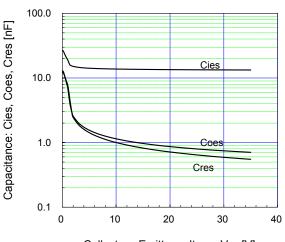
#### ■ Characteristics (Representative)



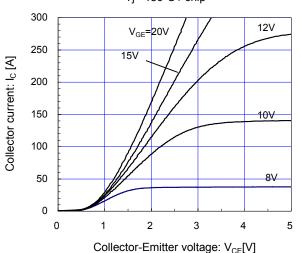
 $[Inverter\ ] \\ Collector\ current\ vs.\ Collector-Emitter\ voltage\ (typ.) \\ V_{GE} = 15V\ /\ chip$ 



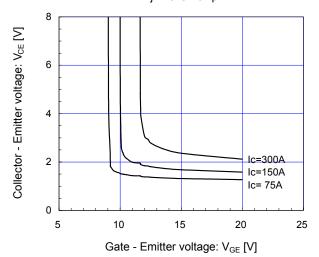
 $\label{eq:continuous} \begin{tabular}{ll} [Inverter] \\ Capacitance vs. Collector-Emitter voltage (typ.) \\ $V_{GE}$=0V, f= 1MHz, Tj= 25°C \\ \end{tabular}$ 



Collector - Emitter voltage:  $V_{CE}\left[V\right]$ 



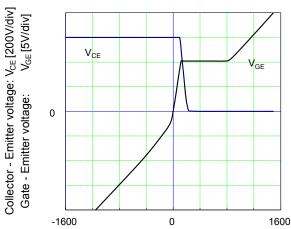
 $\label{eq:continuous} \begin{tabular}{ll} \begin{tabular}{ll} Inverter \cite{beta} & \\ \begin{tabular}{ll} Collector-Emitter voltage vs. Gate-Emitter voltage (typ.) \\ \begin{tabular}{ll} Tj=25^{\circ}C \ / \ chip \end{tabular}$ 



[ Inverter ]

Dynamic gate charge (typ.)

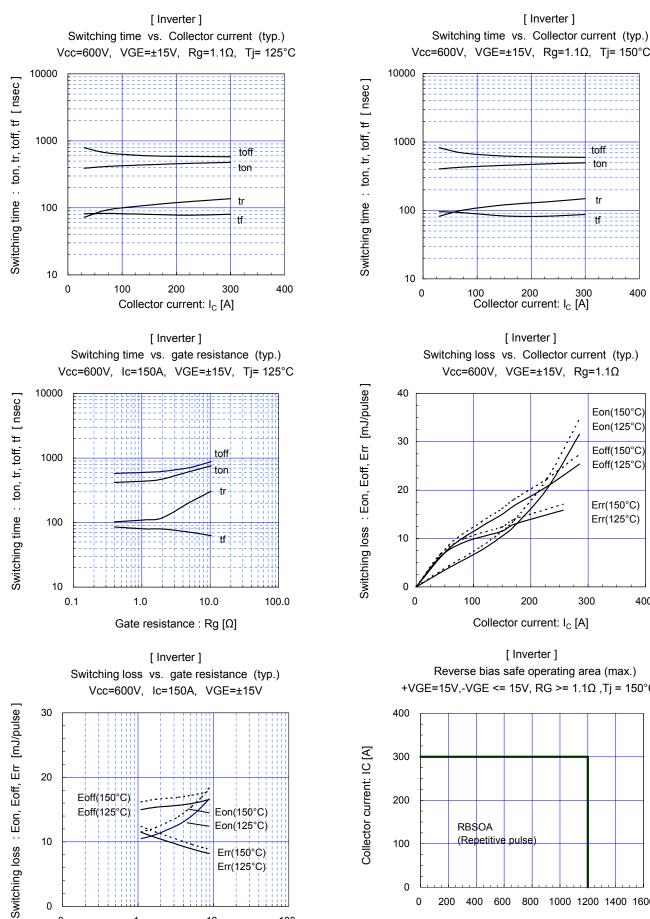
Vcc=600V, Ic=150A, Tj= 25°C



Gate charge: Qg [nC]

0

[Inverter]



Vcc=600V, VGE= $\pm$ 15V, Rg=1.1 $\Omega$ , Tj= 150°C toff ton tr tf  $\begin{array}{ccc} 100 & 200 & 300 \\ \text{Collector current: } I_{\text{C}} \text{ [A]} \end{array}$ 400 300 [Inverter] Switching loss vs. Collector current (typ.) Vcc=600V, VGE= $\pm$ 15V, Rg=1.1 $\Omega$ Eon(150°C) Eon(125°C) Eoff(150°C) Eoff(125°C) Err(150°C) Err(125°C) 200 300 400 Collector current: I<sub>C</sub> [A] [Inverter] Reverse bias safe operating area (max.)  $+VGE=15V,-VGE \le 15V, RG \ge 1.1\Omega, Tj = 150$ °C **RBSOA** (Repetitive pulse) 400 600 800 1000 1200 1400 1600 Collector-Emitter voltage : Vce [V]

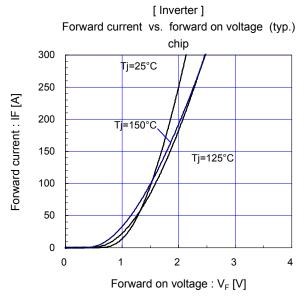
(Main terminals)

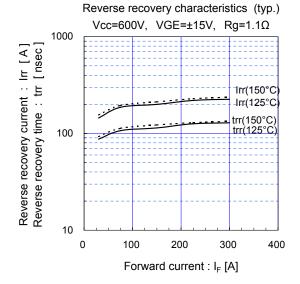
100

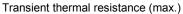
Gate resistance : Rg  $[\Omega]$ 

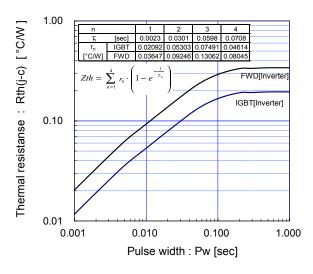
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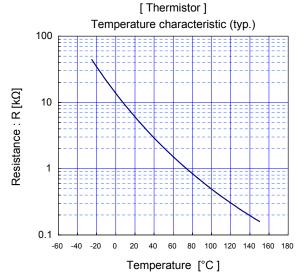
[Inverter]



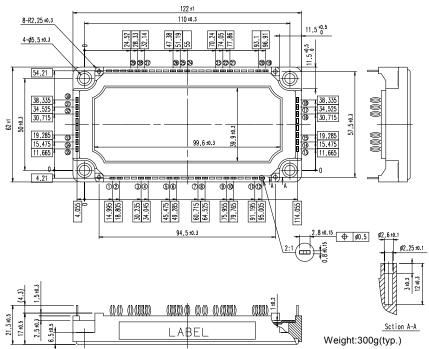








#### ■ Outline Drawings, mm



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