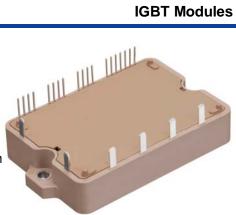


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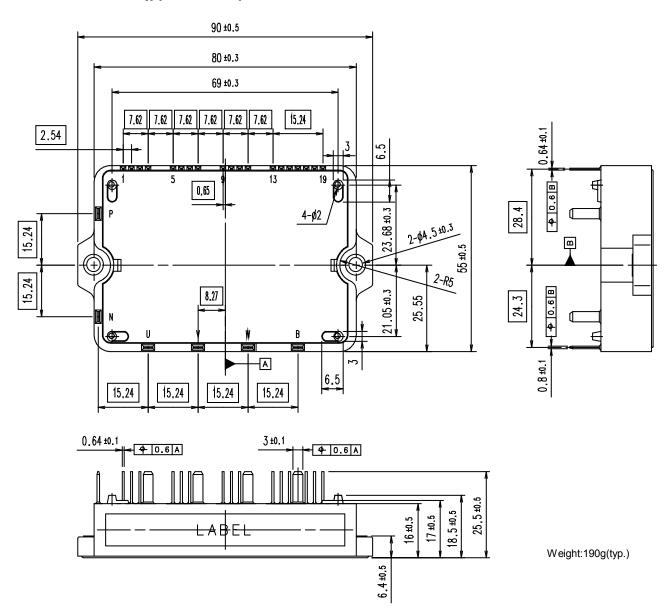
IGBT Module (V series) 600V / 100A / IPM

■ Features

- Temperature protection provided by directly detecting the junction temperature of the IGBTs
- ·Low power loss and soft switching
- ·High performance and high reliability IGBT with overheating protection
- ·Higher reliability because of a big decrease in number of parts in built-in control circuit



■ Outline drawing (Unit: mm)



IGBT Modules

■ Absolute Maximum Ratings

Tc=25°C, Vcc=15V unless otherwise specified.

	Items	Symbol	Min.	Max.	Units	
Co	llector-Emitter Voltage *1	VCES	0	600	V	
Sh	ort Circuit Voltage		Vsc	200	400	V
7		DC	Ic	-	100	Α
Inverter	Collector Current	1ms	ICP	-	200	Α
۱¥		Duty=100% *2	-lc	1	100	Α
_=	Collector Power Dissipation	1 device *3	Pc	-	403	W
4	Collector Current	DC	Ic	1	-	Α
Brake	Collector Current	1ms	ICP	-	-	Α
ΙÄ	Forward Current of Diode	lF	1	_	Α	
	Collector Power Dissipation	1 device *3	Pc	-	-	W
Sι	pply Voltage of Pre-Driver *4		Vcc	-0.5	20	V
Inp	out Signal Voltage *5		Vin	-0.5	Vcc+0.5	V
Αla	arm Signal Voltage *6		VALM	-0.5	Vcc	V
Αla	arm Signal Current *7		IALM	1	20	mΑ
Ju	nction Temperature		Tj	-	150	°C
Op	perating Case Temperature		Topr	-20	110	°C
St	orage Temperature		Tstg	-40	125	°C
Sc	lder Temperature *8		Tsol	1	260	°C
Isc	olating Voltage *9		Viso	-	AC2500	Vrms
Sc	rew Torque	Mounting (M4)	-	-	1.7	Nm

Notes

- *1: VCES shall be applied to the input voltage between terminal P-(U,V, W,B) and (U,V, W,B)-N.
- *2: Duty=125°C/Rth(j-c)D/(IF×VF Max.)×100
- *3: PC=125°C/Rth(j-c)Q (Inverter & Brake)
- *4: VCC shall be applied to the input voltage between terminal No.4 and 1, 8 and 5, 12 and 9,14 and 13.
- *5: Vin shall be applied to the input voltage between terminal No.3 and 1, 7 and 5, 11 and 9,15~18 and 13.
- *6: VALM shall be applied to the voltage between terminal No.2 and 1, 6 and 5, 10 and 9,19 and 13.
- *7: IALM shall be applied to the input current to terminal No.2,6,10 and 19.
- *8: Immersion time 10±1sec. 1time
- *9: Terminal to base, 50/60Hz sine wave 1min. All terminals should be connected together during the test.

■ Electrical Characteristics (Tj=25°C, VCC=15V unless otherwise specified.)

Main circuit

	Item	Symbol		(Conditions	S	Min.	Тур.	Max.	Units		
-e	Collector Current at off signal input	ICES	VCE	=600V			1	ı	1.0	mA		
Inverter	Collector-Emitter	VCE(sat)	Ic	= 100A		Terminal	-	-	1.95	V		
≥	saturation voltage	V CL(Sat)				Chip	-	1.25	-	V		
<u>-</u>	Forward voltage of FWD	VF	lF	= 100A		Terminal	-	ı	2.2	V		
	Forward voitage of FVVD					Chip	ı	1.6	ı	V		
(I)	Collector Current at off signal input	ICES	VCE	=			1	1	ı	mA		
Brake	Collector-Emitter	VCE(sat)	VCE(sat)	Ic	=		Terminal	-	-	-	V	
32	saturation voltage			v o∟(sat)	v o∟(sat)	v C∟(Sat)				Chip	ı	ı
1 "	Forward voltage of FWD	VF	lF	=		Terminal	ı	ı	1	V		
	Forward voitage of FVVD					Chip	ı	ı	ı	V		
		ton	VDC	= 300V	, Tj=12	5°C	1.1	ı	1	μs		
Cwitchir	hing time	toff	IC	= 100A			-	-	2.1	μs		
SWILC	illing time	trr	VDC IF	= 300V = 100A			- 1	ı	0.3	μs		



IGBT Modules

Control circuit

Item	Symbol	Conditions	S	Min.	Тур.	Max.	Units
Supply current of P-side pre-driver (per one unit)	Ісср	Switching Frequency = 0-15kHz Tc=-20~110°C		ı	ı	17	mA
Supply current of N-side pre-driver	Iccn			ı	ı	51	mA
Input signal threshold voltage	Vinth(on)	Vin-GND	ON	1.2	1.4	1.6	V
Vinth(off)		VIII-GIND	OFF	1.5	1.7	1.9	V

Protection Circuit

Item		Symbol		Cond	ditions		Min.	Тур.	Max.	Units
Over Current	Inverter	loc	Tj=125°C				200	-	-	Α
Protection Level	Brake	100	•		Resista	nce Load	-	-	-	Α
Over Current Protection	Delay time	tdOC	Tj=125°C				-	5	ı	μs
Short Circuit Protection [Delay time	tsc	Tj=125°C				-	2	3	μs
IGBT Chips Over Heating	g	Тјон	Surface of				150	-	-	°C
Protection Temperature	Level	ПОП	IGBT Chips	;						
Over Heating Protection	Hysteresis	TjH					-	20	ı	°C
Under Voltage Protection	n Level	Vuv					11.0	-	12.5	V
Under Voltage Protection	n Hysteresi	VH					0.2	0.5	ı	V
		tALM(OC)	ALM-GND				1.0	2.0	2.4	ms
Alarm Signal Hold Time		talm(UV)	Tc=-20~11	٥°C	V	/cc≧10V	2.5	4.0	4.9	ms
		talm(Tjoh)	1020~11	UC			5.0	8.0	11.0	ms
Resistance for current lin	nit	RALM					960	1265	1570	Ω

■ Thermal Characteristics (Tc = 25°C)

Item			Symbol	Min.	Тур.	Max.	Units
	Inverter	IGBT	Rth(j-c)Q	1	ı	0.31	°C/W
Junction to Case	IIIVEITEI	FWD	Rth(j-c)D	ı	ı	0.55	%C/W
Thermal Resistance*10	Brake	IGBT	Rth(j-c)Q	-	-	-	°C/W
Diake		FWD	Rth(j-c)D	-	-	-	°C/W
Case to Fin Thermal Res	Rth(c-f)	-	0.05	-	°C/W		

^{*10:} For 1device, the measurement point of the case is just under the chip.

■ Noise Immunity (VDC=300V, VCC=15V)

Item	Conditions	Min.	Тур.	Max.	Units
Common mode	Pulse width 1µs,polarity ±,10min.	±2.0	-	-	kV
rectangular noise	Judge: no over-current, no miss operating				

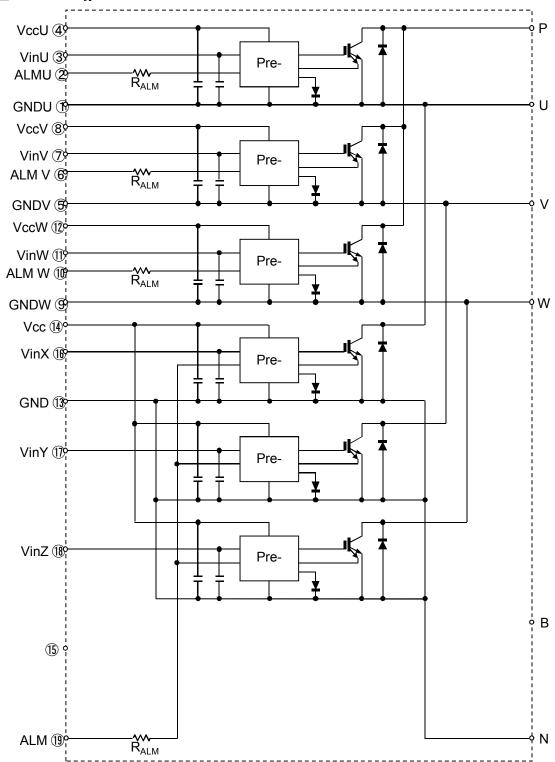
■ Recommended Operating Conditions

Item	Symbol	Min.	Тур.	Max.	Units
DC Bus Voltage	VDC	1	-	400	V
Power Supply Voltage of Pre-Driver	Vcc	13.5	15.0	16.5	V
Switching frequency of IPM	fsw	1	-	20	kHz
Arm shoot through blocking time for IPM's input signal	tdead	1.0	-	1	μs
Screw Torque (M4)	-	1.3	-	1.7	Nm



IGBT Modules

■ Block Diagram



Pre-drivers include following functions

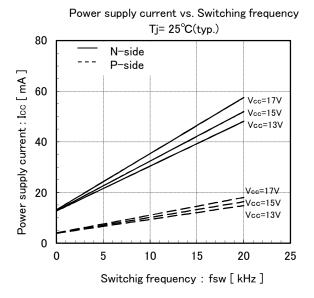
- 1. Amplifier for driver
- 2. Short circuit protection
- 3. Under voltage lockout circuit
- 4. Over current protection
- 5. IGBT chip over heating protection

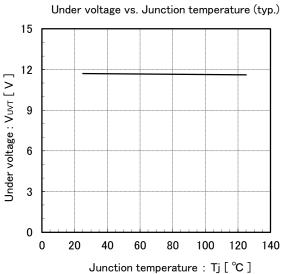


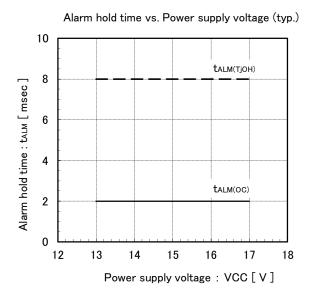
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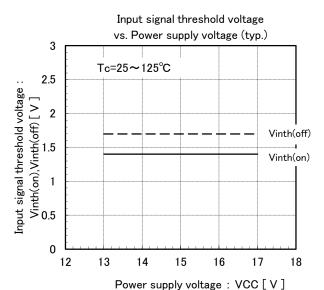
■ Characteristics (Representative)

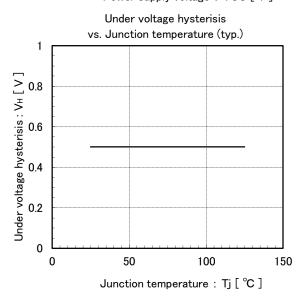
Control Circuit

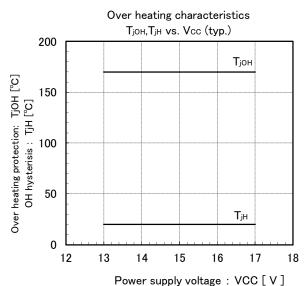






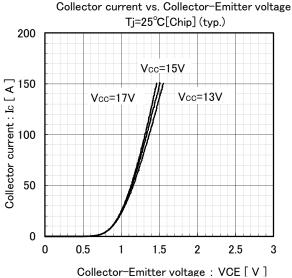




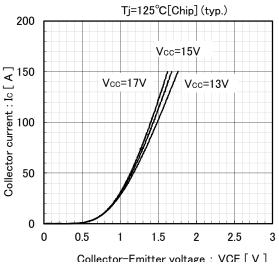


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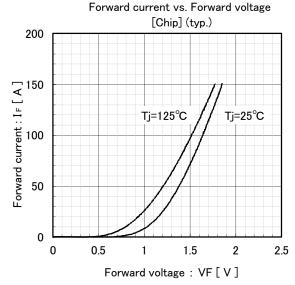
Inverter



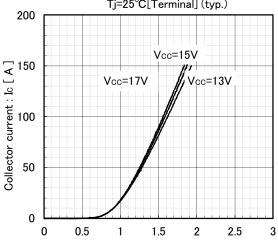
Collector current vs. Collector-Emitter voltage



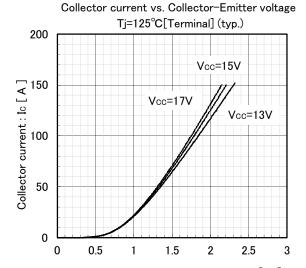
Gollector-Emitter voltage: VGE I V



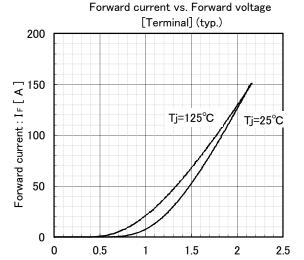
Collector current vs. Collector-Emitter voltage Tj=25°C[Terminal] (typ.)



Collector-Emitter voltage : VCE $\lceil V \rceil$

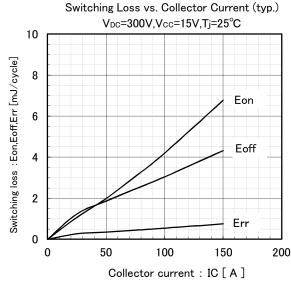


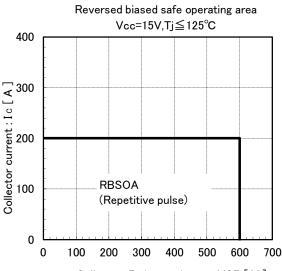
Collector-Emitter voltage: VCE [V]

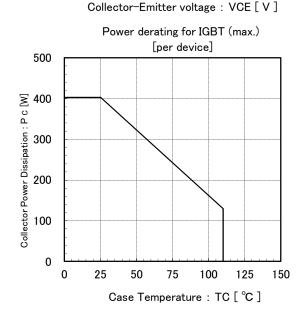


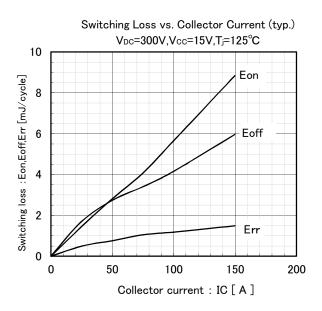
Forward voltage : VF [V]

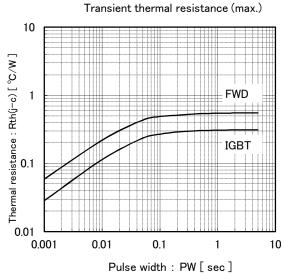
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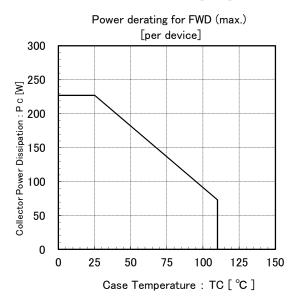






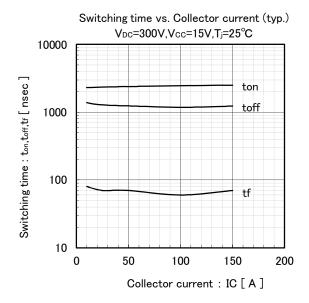


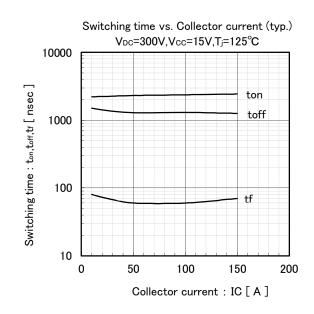


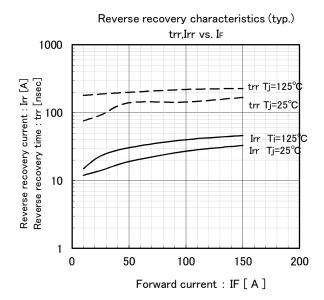


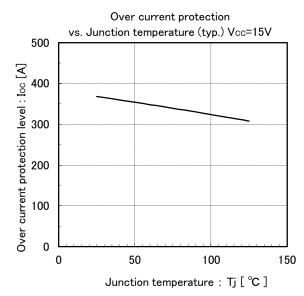


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