

<IGBT Modules>

CM200DY-34T

HIGH POWER SWITCHING USE INSULATED TYPE



dual switch (half-bridge)

Collector current I_C 2 0 0 A Collector-emitter voltage Vces 1 7 0 0 V Maximum junction temperature T_{vjmax}

- •Flat base type
- Copper base plate (Nickel-plating)
- Nickel-plating tab terminals
- •RoHS Directive compliant
- •UL Recognized under UL1557, File No.E323585

APPLICATION

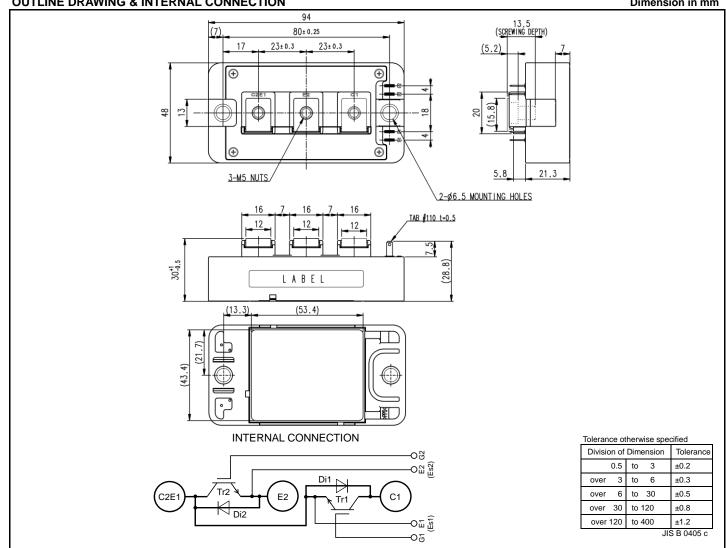
AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

- •PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V_{CEsat} selection for parallel connection

OUTLINE DRAWING & INTERNAL CONNECTION

Dimension in mm



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INSULATED TYPE

MAXIMUM RATINGS (T_{vj} =25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Callagton augment	DC, Tc=134 °C* (Note2, 4)	200	^	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	400	Α	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	2270	W	
I _E (Note1)	Facilities account	DC (Note2)	200	^	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	400	Α	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V	
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload)	175	°C	
T _{Cmax}	Maximum case temperature				
T _{vjop}	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	°C	
T _{stg}	Storage temperature	-	-40 ~ +150*		

ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	Itom	ltom Conditions		Limits			Linit
	Item	Conditions	Conditions		Тур.	Max.	Unit
CES	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	m/
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =20 mA, V _{CE} =10 V		5.4	6.0	6.6	V
	J	I _C =200 A, V _{GE} =15 V,	T _{vj} =25 °C	-	2.05	2.50	V
V _{CEsat}		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.50	-	
(Terminal)		(Note5)	T _{vj} =150 °C	-	2.60	-	
	Collector-emitter saturation voltage	I _C =200 A,	T _{vj} =25 °C	-	1.95	2.35	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	2.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.45	-	
Cies	Input capacitance			-	-	55	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited		-	-	1.4	nF
Cres	Reverse transfer capacitance			-	-	0.5	1
Q _G	Gate charge	V _{CC} =1000 V, I _C =200 A, V _{GE} =15 V		-	1.6	-	μC
t _{d(on)}	Turn-on delay time	V_{CC} =1000 V, I_{C} =200 A, V_{GE} =±15 V, R_{G} =0 Ω, Inductive load		-	-	800	
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	ns
t _f	Fall time			-	-	600	
(Note 4)		I _E =200 A, G-E short-circuited,	T _{vj} =25 °C	-	2.75	3.35	V
V _{EC} (Note.1)		Refer to the figure of test circuit	T _{vj} =125 °C	-	3.00	-	
(Terminal)		(Note5)	T _{vj} =150 °C	-	3.00	-	
(Note 4)	- Emitter-collector voltage	I _E =200 A,	T _{vj} =25 °C	-	2.65	3.20	
V _{EC} (Note.1)		G-E short-circuited,	T _{vj} =125 °C	-	2.75	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.75	-	
t _{rr} (Note1)	Reverse recovery time	V _{CC} =1000 V, I _E =200 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	$R_G=0 \Omega$, Inductive load		-	10	-	μ(
Eon	Turn-on switching energy per pulse	V_{CC} =1000 V, I_{C} = I_{E} =200 A, V_{GE} =±15 V, R_{G} =0 Ω , $T_{v_{j}}$ =150 °C,		-	56.3	-	
E _{off}	Turn-off switching energy per pulse			-	52.4	-	m
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load		-	22.7	-	m
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C (Note4)		-	0.3	-	m!
r _g	Internal gate resistance	Per switch		-	3.8	-	Ω

^{*:} The value of PC-TIM applied module is limited by the heat resistant temperature of PC-TIM.

HIGH POWER SWITCHING USE

INSULATED TYPE

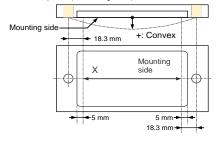
THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
	item	Conditions	Min.	Тур.	Max.	Offit
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	66.0	K/kW
$R_{th(j-c)D}$	Thermai resistance	Junction to case, per Inverter FWD (Note4)	-	-	91.9	r/KVV
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4, 6)	-	24.0	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions		Limits			Llait
				Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N⋅m
ds	Creepage distance	Terminal to terminal		18	-	-	- mm
		Terminal to base plate		21.1	-	-	
da	Classes	Terminal to terminal		9.6	-	-	
	Clearance	Terminal to base plate		16.7	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Note7)		±0	-	+200	μm
m	mass	-		-	155	-	g

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
 - 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed $T_{vj\,m\,a\,x}$ rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
 - 6. Typical value is measured by using thermally conductive grease of λ =3.0 W/(m·K)/D_(C-S)=50 μ m.
 - 7. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



HIGH POWER SWITCHING USE

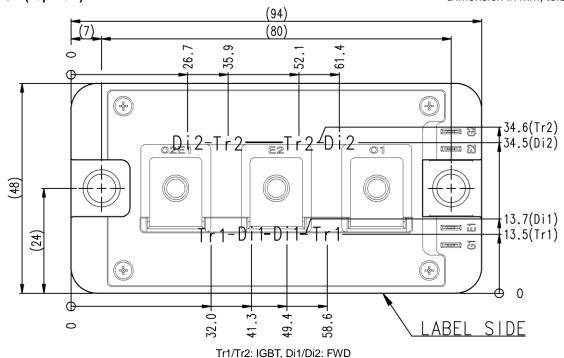
INSULATED TYPE

RECMENDED OPERATING CONDITIONS

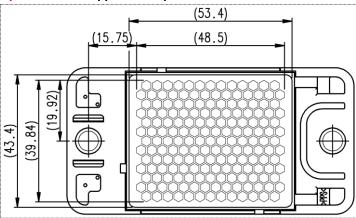
Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Offic
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals	-	1000	1200	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	0	-	39	Ω

CHIP LOCATION (Top view)

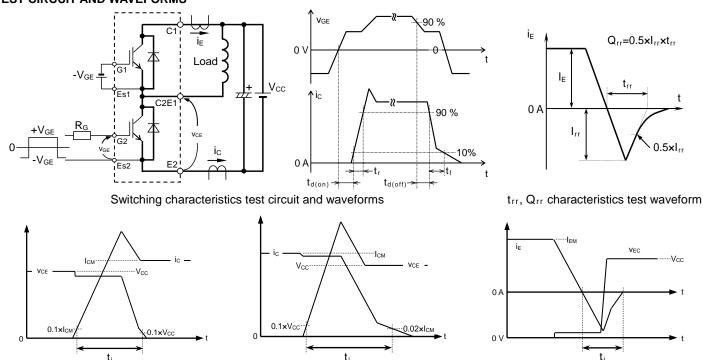
Dimension in mm, tolerance: ±1 mm



Option: PC-TIM applied baseplate outline



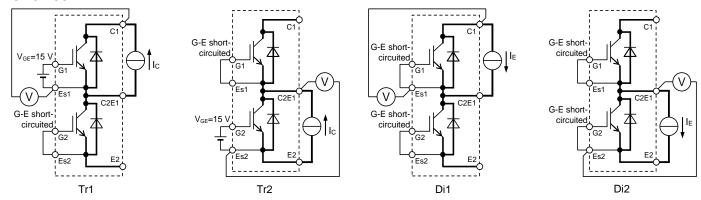
TEST CIRCUIT AND WAVEFORMS



IGBT Turn-off switching energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT

IGBT Turn-on switching energy



V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

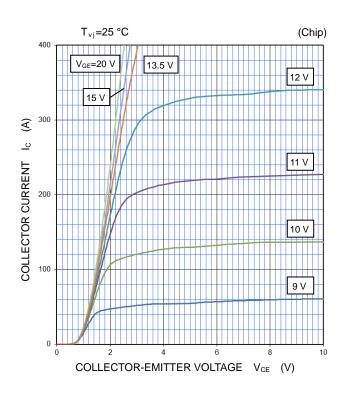
FWD Reverse recovery energy

HIGH POWER SWITCHING USE

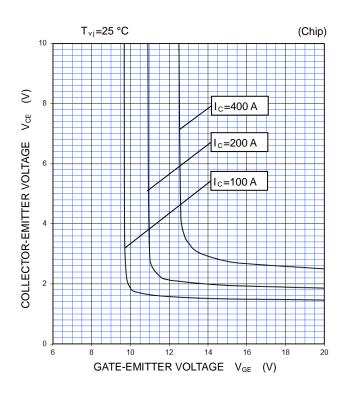
INSULATED TYPE

PERFORMANCE CURVES

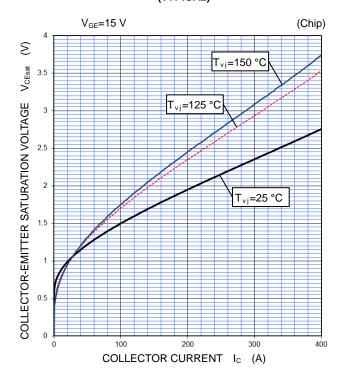
OUTPUT CHARACTERISTICS (TYPICAL)



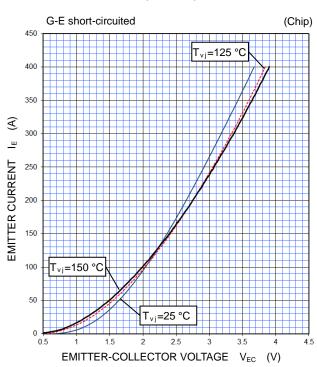
COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)

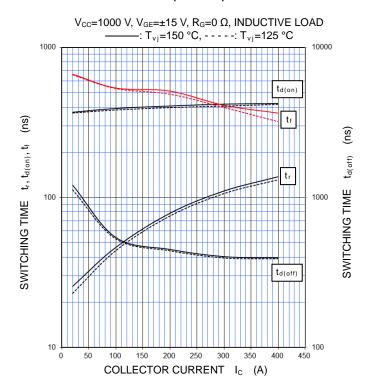


FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

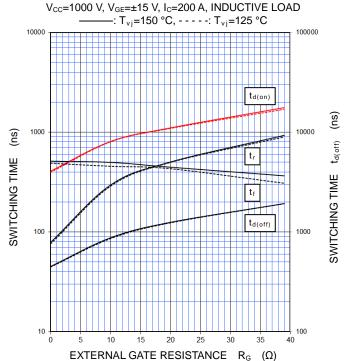


PERFORMANCE CURVES

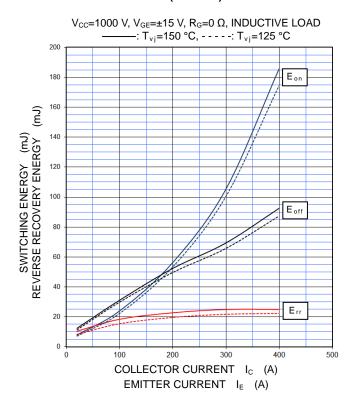
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



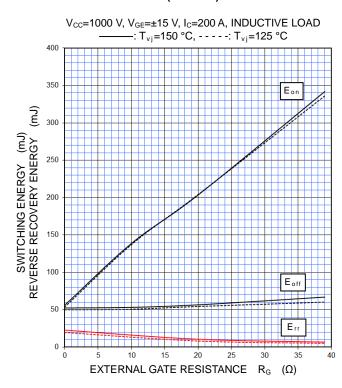
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

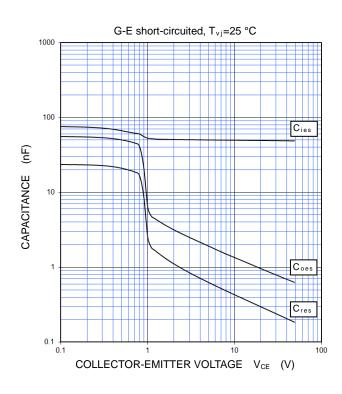


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

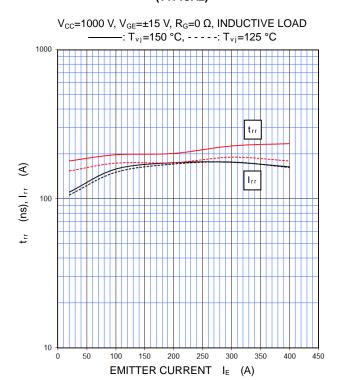


PERFORMANCE CURVES

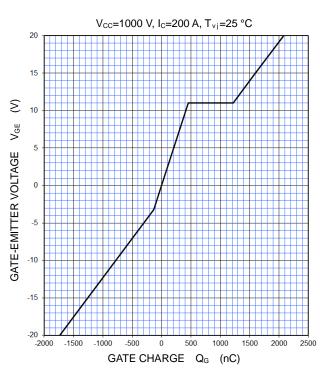
CAPACITANCE CHARACTERISTICS (TYPICAL)



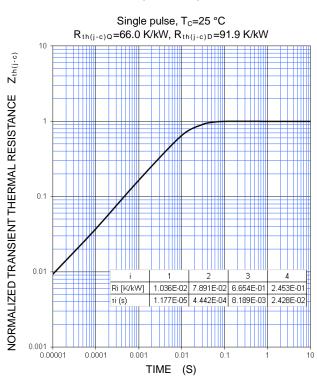
FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



GATE CHARGE CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



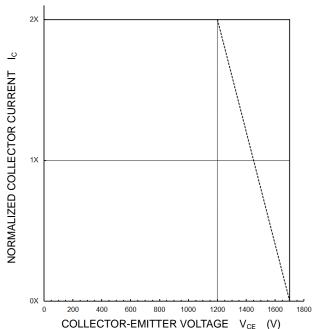
Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE

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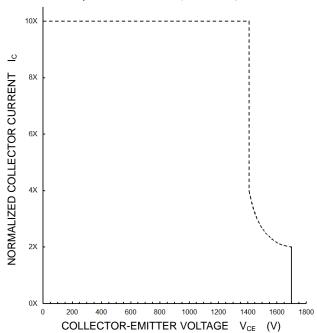
PERFORMANCE CURVES

TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 1200$ V, $V_{GE} = \pm 15$ V, $R_G = 0 \sim 39$ Ω , $T_{vj} = 25 \sim 150$ °C, $t_W \le 8$ μs , Non-Repetitive



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INSULATED TYPE

Keep safety first in your circuit designs!

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Publication Date : May 2018 CMH-11389-B Ver.1.2