HIGH POWER SWITCHING USE INSULATED TYPE

#### CM600DU-12NFH



**Dual (Half-Bridge)** 

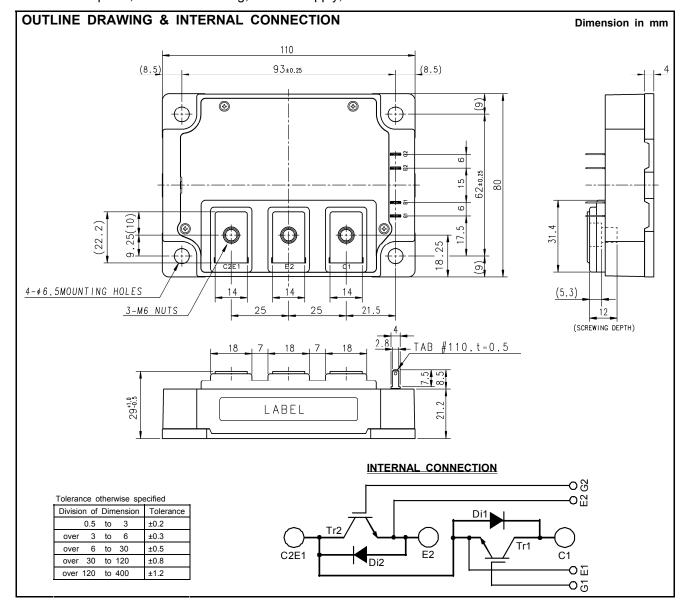
- 5th generation Fast switching IGBT module -

Maximum junction temperature T<sub>imax</sub> ... **150** °C

- •Flat base Type
- •Copper base plate
- •RoHS Directive compliant
- •UL Recognized under UL1557, File E323585

#### **APPLICATION**

High frequency (30 kHz ~ 60 kHz) switching use: Gradient anplifier, Induction heating, Power supply, etc.





HIGH POWER SWITCHING USE INSULATED TYPE

ABSOLUTE MAXIMUM RATINGS (T<sub>j</sub>=25 °C, unless otherwise specified)

	` ,	• • • • • • • • • • • • • • • • • • • •			
Symbol	Item	Conditions	Rating	Unit	
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	600	V	
V <sub>GES</sub>	Gate-emitter voltage	C-E short-circuited	±20	V	
Ic		Operation (Note.5)	600		
I <sub>C(rms)</sub>	Collector current	Operation	400	Α	
I <sub>CRM</sub>		Pulse, Repetitive (Note.4)	1200		
P <sub>tot</sub>	Total newer dissination	T <sub>C</sub> =25 °C (Note.2, 5)	1130	W	
P <sub>tot</sub> '	Total power dissipation	T <sub>C</sub> '=25 °C (Note.3, 5)	2350	VV	
I <sub>E</sub> (Note.1)		Operation (Note.5)	600		
I <sub>E(rms)</sub> (Note.1)	Emitter current (Free wheeling diode forward current)	Operation	400	Α	
I <sub>ERM</sub> (Note.1)	(1 ree wheeling aloae forward current)	Pulse, Repetitive (Note.4)	1200		
Tj	Junction temperature	-	-40 ~ +150	°C	
T <sub>stg</sub>	Storage temperature	-	-40 ~ +125		
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V	

#### ELECTRICAL CHARACTERISTICS (T<sub>j</sub>=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
Symbol	item			Min.	Тур.	Max.	Offic
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1	mA
I <sub>GES</sub>	Gate-emitter leakage current	±V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circ	uited	-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I <sub>C</sub> =60 mA, V <sub>CE</sub> =10 V		5	6	7	V
V	Collector emitter acturation valtage	I <sub>C</sub> =600 A (Note.6),	T <sub>j</sub> =25 °C	-	2.0	2.7	V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	V <sub>GE</sub> =15 V	T <sub>j</sub> =125 °C	-	1.95	-	V
Cies	Input capacitance			-	-	166	nF
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circu	ited	-	-	11	
Cres	Reverse transfer capacitance	1		-	-	6.0	İ
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =15 V		-	3720	-	nC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =300 V, I <sub>C</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	650	ns
t <sub>r</sub>	Rise time			-	-	250	
t <sub>d(off)</sub>	Turn-off delay time	R <sub>G</sub> =2.0 Ω, Inductive load		-	-	800	
t <sub>f</sub>	Fall time			-	-	150	
V <sub>EC</sub> (Note.1)	Emitter-collector voltage	I <sub>E</sub> =600 A (Note.6), G-E short-circuited		-	2.0	2.6	V
t <sub>rr</sub> (Note.1)	Reverse recovery time	V <sub>CC</sub> =300 V, I <sub>E</sub> =600 A, V <sub>GE</sub> =±15 V,		-	-	200	ns
Q <sub>rr</sub> (Note.1)	Reverse recovery charge	$R_G$ =2.0 $\Omega$ , Inductive load	d	-	11	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =300 V, I <sub>C</sub> =I <sub>E</sub> =600 A	,	-	11	-	
E <sub>off</sub>	Turn-off switching energy per pulse	$V_{GE}$ =±15 V, $R_{G}$ =2.0 $\Omega$ , 1	<sub>j</sub> =125 °C,	-	27	-	mJ
E <sub>rr</sub> (Note.1)	Reverse recovery energy per pulse	Inductive load		-	6.3	-	ļ
r <sub>g</sub>	Internal gate resistance	Per switch, T <sub>C</sub> =25 °C		-	0.8	-	Ω

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance (Note.2)	Junction to case, per IGBT	-	-	0.11	K/W
$R_{th(j-c)D}$		Junction to case, per FWDi	-	-	0.12	K/W
R <sub>th(c-s)</sub>	Contact thermal resistance (Note.2)	Case to heat sink, per 1/2 module, Thermal grease applied (Note.7)	ı	0.02	-	K/W
$R_{th(j-c')Q}$	Thermal resistance (Note.3)	Junction to case, per IGBT	-	-	53	K/kW
$R_{th(j-c')D}$		Junction to case, per FWDi	-	-	78	K/kW

#### **MECHANICAL CHARACTERISTICS**

Symbol	Item	Conditions	Limits			Unit
Symbol		Conditions	Min.	Тур.	Max.	Offic
$M_t$	Mounting torque	Main terminals M 6 screw	3.5	4.0	4.5	N·m
Ms		Mounting to heat sink M 6 screw	3.5	4.0	4.5	INTIII
m	Weight	-	-	580	-	g
e <sub>c</sub>	Flatness of base plate	On the centerline X, Y (Note.8)	-100	-	+100	μm



February-2011

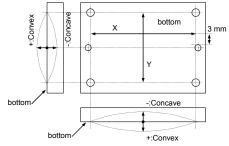
HIGH POWER SWITCHING USE INSULATED TYPE

RECOMMENDED OPERATING CONDITIONS (Ta=25 °C)

Symbol	Item	Conditions	Limits			Unit
			Min.	Тур.	Max.	Offic
V <sub>CC</sub>	(DC) Supply voltage	Applied across C1-E2	-	300	400	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2	13.5	15.0	16.5	V
$R_G$	External gate resistance	Per switch	1.0	-	10	Ω

- Note.1: Represent ratings and characteristics of the anti-parallel, emitter-collector free wheeling diode (FWDi).
- Note.2: Case temperature  $(T_C)$  measured point is base plate side. (Refer to the figure of chip location)
- Note.3: Case temperature ( $T_c$ ') and heat sink temperature ( $T_s$ ') are defined on the each surface of base plate and heat sink just under the chips. (Refer to the figure of chip location)

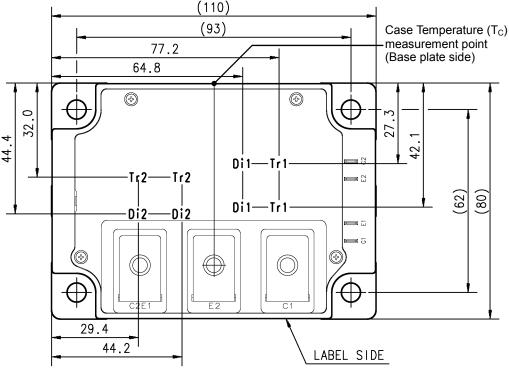
  The heat sink thermal resistance { $R_{th(s-a)}$ } should measure just under the chips.
- Note 4: Pulse width and repetition rate should be such that the device junction temperature (T<sub>i</sub>) dose not exceed T<sub>imax</sub> rating.
- Note.5: Junction temperature  $(T_j)$  should not increase beyond  $T_{jmax}$  rating.
- Note.6: Pulse width and repetition rate should be such as to cause negligible temperature rise. (Refer to the figure of test circuit)
- Note.7: Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K).
- Note.8: Base plate flatness measurement points are as in the following figure.



Note.9: No short circuit capability is designed.

#### **CHIP LOCATION (Top view)**

Dimension in mm, tolerance: ±1 mm

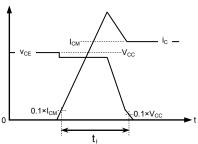


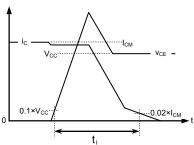
Tr1/Tr2: IGBT, Di1/Di2: FWDi

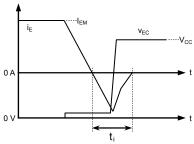


HIGH POWER SWITCHING USE INSULATED TYPE

#### **TEST CIRCUIT AND WAVEFORMS** Short-circuited Short-Shortcircuited circuited C2E1 C2E1 Short-Short-circuited circuited Shortcircuited Tr1 Tr2 Di1 Di2 V<sub>CEsat</sub> test circuit $V_{\text{EC}}$ test circuit ≬ V<sub>GE</sub> $Q_{rr}=0.5\times I_{rr}\times t_{rr}$ 0 V Load 😂 $I_{\mathsf{E}}$ 0 A 90 % $I_{rr}$ $t_{rr},\ Q_{rr}$ test waveform Switching characteristics test circuit and waveforms







IGBT Turn-on switching energy

IGBT Turn-off switching energy

FWDi Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

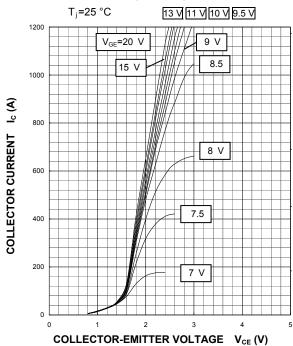


HIGH POWER SWITCHING USE INSULATED TYPE

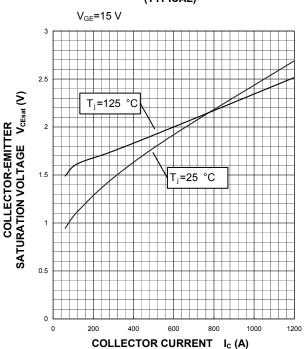
### PERFORMANCE CURVES

#### **INVERTER PART**

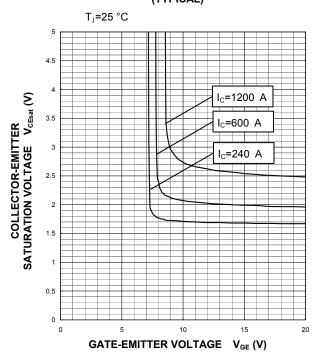
# OUTPUT CHARACTERISTICS (TYPICAL)



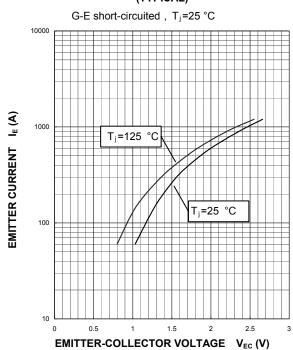
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

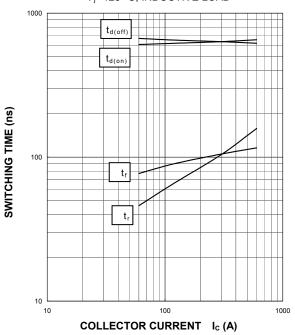




HIGH POWER SWITCHING USE INSULATED TYPE

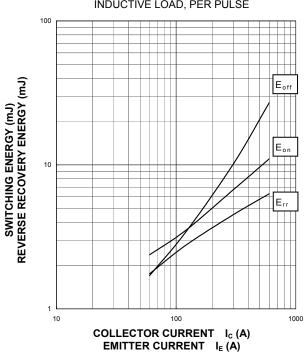
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =300 V,  $V_{GE}$ =±15 V,  $R_{G}$ =2.0  $\Omega$ ,  $T_{i}$ =125 °C, INDUCTIVE LOAD



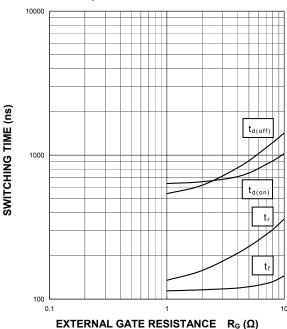
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =300 V,  $V_{GE}$ = $\pm$ 15 V,  $R_{G}$ =2.0  $\Omega$ ,  $T_{j}$ =125 °C, INDUCTIVE LOAD, PER PULSE



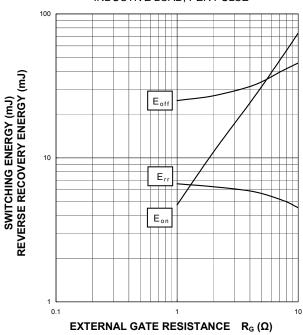
#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =300 V,  $I_{C}$ =600 A,  $V_{GE}$ =±15 V,  $T_{j}$ =125 °C, INDUCTIVE LOAD



#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{CC}$ =300 V,  $I_C/I_E$ =600 A,  $V_{GE}$ =±15 V,  $T_j$ =125 °C, INDUCTIVE LOAD, PER PULSE

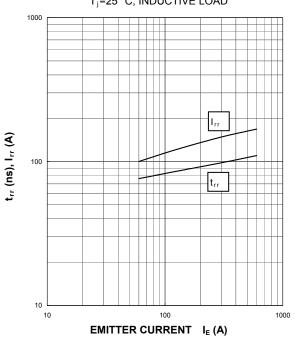




HIGH POWER SWITCHING USE INSULATED TYPE

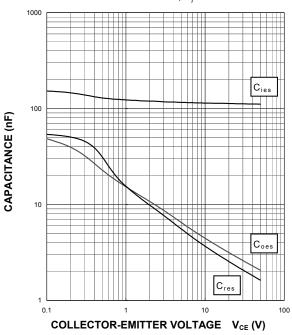
# FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)

 $V_{\text{CC}}$ =300 V,  $V_{\text{GE}}$ =±15 V,  $R_{\text{G}}$ =2.0  $\Omega$ ,  $T_{\text{j}}$ =25 °C, INDUCTIVE LOAD



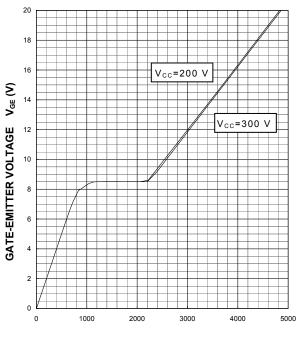
# CAPACITANCE CHARACTERISTICS (TYPICAL)

G-E short-circuited,  $T_j$ =25 °C



# GATE CHARGE CHARACTERISTICS (TYPICAL)

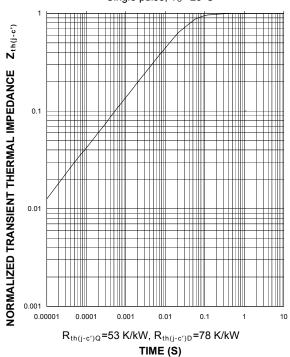
I<sub>C</sub>=600 A, T<sub>j</sub>=25 °C



GATE CHARGE Q<sub>G</sub> (nC)

#### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)

Single pulse, T<sub>C</sub>'=25°C





# MITSUBISHI IGBT MODULES CM600DU-12NFH

HIGH POWER SWITCHING USE INSULATED TYPE

#### Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage.

Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

#### Notes regarding these materials

- These materials are intended as a reference to assist our customers in the selection of the Mitsubishi semiconductor product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Mitsubishi Electric Corporation or a third party.
- Mitsubishi Electric Corporation assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
- All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Mitsubishi Electric Corporation without notice due to product improvements or other reasons. It is therefore recommended that customers contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for the latest product information before purchasing a product listed herein. The information described here may contain technical inaccuracies or typographical errors. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability, or other loss rising from these inaccuracies or errors.
- Please also pay attention to information published by Mitsubishi Electric Corporation by various means, including the Mitsubishi Semiconductor home page (http://www.mitsubishichips.com/Global/index.html).
- When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information as a total system before making a final decision on the applicability of the information and products. Mitsubishi Electric Corporation assumes no responsibility for any damage, liability or other loss resulting from the information contained herein.
- ·Mitsubishi Electric Corporation semiconductors are not designed or manufactured for use in a device or system that is used under circumstances in which human life is potentially at stake. Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor when considering the use of a product contained herein for any specific purposes, such as apparatus or systems for transportation, vehicular, medical, aerospace, nuclear, or undersea repeater use.
- ·The prior written approval of Mitsubishi Electric Corporation is necessary to reprint or reproduce in whole or in part these materials.
- If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination.
- Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited.
- ·Please contact Mitsubishi Electric Corporation or an authorized Mitsubishi Semiconductor product distributor for further details on these materials or the products contained therein.



February-2011